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• Database *From:* "Phil Covington" <phil@...>

[phil at vhm](#)

• Polls *Date:* Tue Oct 4, 2005 8:00 pm

[Offline](#)

• Members *Subject:* Welcome to the Open_High_Performance_SDR Group

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• Calendar

Hello all,

• Promote Very soon I will be posting much more details on the HPSDR project.

The latest info will always be at:

<http://pcovington.blogspot.com/>

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The current status is that I am working on the firmware for the EZUSB FX2LP microcontroller to allow downloading configuration data to the FPGA. I am using the open source SDCC compiler to program the FX2LP.

Did you know...

73 de Phil N8VB

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Replies

Author

Date

Ken - N9VV

[n9vv](#)

Oct 4, 2005

8:15 pm



[Re: Welcome to the Open_High_Performance_SDR Group](#)

Dear Mr. Covington, Thank you for your warm welcome to this exciting new group.

Your innovative design brings great promise for the future of Open Source SDR....

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From: Ken - N9VV <n9vv@...>

Date: Tue Oct 4, 2005 8:14 pm

Subject: re: Welcome to the Open_High_Performance_SDR Group

[n9vv](#)

 [Offline](#)

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Dear Mr. Covington,

Thank you for your warm welcome to this exciting new group. Your innovative design brings great promise for the future of Open Source SDR. I can imagine various plug-in "personality" modules that could make the unit a very high resolution spectrum analyzer or a Ham Radio rig of exceptionally high performance and quality. Thank you for sharing your success and research with the group.

Ken

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Did you know...

> Hello all,
>

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> Very soon I will be posting much more details on the HPSDR project.
> The latest info will always be at:
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> 73 de Phil N8VB

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From: "Ken Hopper" <n9vv@...> [n9vv](#)

Date: Wed Oct 5, 2005 6:04 pm [Offline](#)

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Subject: I shortened the header [Send Email](#)

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Dear group members,

● Calendar

when I saw the first two messages I realized that the header was

● Promote

exceptionally long. I shortened (abbreviated) it to Open_HPSSDR for your viewing pleasure.

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I also added 27 links to interesting web resources related to C#, mini-ITX computers, Windows, Linux, and links to Phil's info.

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I hope you are enjoying our first efforts toward the exciting High Performance SDR goals.

Did you know... Ken

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From: "Phil Covington" <phil@...> [phil at vhmi](mailto:phil@vhmi)

Date: Thu Oct 6, 2005 2:59 pm [Offline](#)

● Polls

Subject: More info...

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Hi all,

● Promote

This is a response to a few questions that someone posted on my blog page requesting more info about the project. I have copied it here since it kind of gives a quick outline of where I am going:

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I saw your comment on my blog page. Thanks for the link.

Did you know...

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I am using libusb-win32 which is a port of the open source libusb project for Linux. My intention is to use the libusb API since it is the same on Windows as it is on Linux. On Windows it consists of a device driver and an interface DLL. They have an inf file generator that will create an .inf file for your device. I am having very good luck with libusb.

link in the date column. Your preferences will be remembered, so you don't have to do it again when you return.

I have written a C#.NET library to interface to the libusb-win32 DLL. It works under the Mono framework in both Windows (using the libusb-win32 driver and DLL) and Linux (using the libusb shared library). It is pretty cool to be able to write software and have it work without recompiling on both Win and Linux platforms.

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For the compiler I want to use the open source SDCC compiler. It assumes a standard 8051 type device so it does not directly support the additional features of the FX2. Basically I have to create custom interrupt vector and usb descriptor tables in assembly modules and link them with the main c firmware. Luckily the SDCC linker allows you to define where in memory particular user defined segments are based so it turned out to be relatively easy one I pieced together all the bits of information from various sources on the internet.

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The only problem that I am having now is that I cannot get the SUDPTR's (that you use to send the usb configuration descriptors) to work. I load them with the correct address but invalid data is returned. I think that there is a problem with the xdata initialization in SDCC's startup code because it assumes a standard 8051. I am working on a replacement .startup8051.asm for the FX2 to correct the deficiencies. The device will not re-enumerate correctly because of the SUDPTR problem. [Edit: As of 10-05-2005 this problem has been resolved and the board re-enumerates correctly - N8VB]

Once I get the re-enumeration fixed, I need to complete the firmware to support uploading the FPGA's configuration data.

I apologize for not responding to your earlier comment on my blog about having a block diagram for the FPGA_USB board. I just saw your message today - I guess I missed it. I've been busy in every spare moment writing support code and firmware for the FPGA_USB board that I have not released much info on it. Basically the FPGA_USB board is a high speed I/O system. It has an I2C bus, an SPI bus, and about 64 lines of bit addressable I/O. It has six 40 pin header receptacles on the board to allow interfacing. The I2C and SPI buses are common to five of the six headers. The remaining pins on the first five headers also contain power and GP I/O lines that can be configured either for CS or ENs for the SPI bus or as general purpose bit addressable I/O. The first and third headers also have individual I2S sound buses to allow connection to the audio A/Ds and D/As. If the I2S buses are not needed those lines can be reconfigured as GP I/O by the FPGA. The sixth header contains all bit addressable GP I/O that is not common to the first five headers. The plug in boards will make the project a SDR, test instrument, etc... For SDR use I plan on having a RX board (with PCM4202 A/D) in the first slot, a DDS board in the second slot, a TX board (with A/D - D/A) in the third slot, a BPF board in the fourth slot, and general purpose I/O interface board in the fifth slot. I want to use a mini-itx board running Linux that will interface via USB to the FPGA_USB board and allow you to make a stand alone radio. The GP I/O will be used for interface to the front panel encoder knobs, switches, indicators, etc... It will also support running in PC connected mode like the SDR-1000 with or without the mini-itx board. I would like to offload some of the DSP to the FPGA also (like the I/Q correction and NCO for starters). I am very interested in some of the open cores for the FPGAs that implement your own custom DSP processor. It would be interesting to see if all the DSP could be done in the FPGA or in maybe a add on FGPA based co-processor board.

If I ever get all the above done I would then like to make a high speed 16 bit A/D board that would replace the TX board and allow you to scan a large swath of spectrum. Then the narrow bandwidth RX board (using the QSD and audio A/D) would tune into interesting signals. I have a sample of a 16 bit 80MSPS A/D from Analog Devices that I will use. The same board could also be the basis for a high speed DSO or logic analyzer project.

The inspiration for the FPGA_USB board was to have an expandable high speed I/O platform to build other projects on. In addition to the FX2

and Spartan 3 FPGA there is a 128 K static ram for the FX2, a 64 K EEPROM, a Cypress I2C programmable clock generator chip with 6 clock outputs, and regulators (3.3V, 2.5V, and 1.2V). The board requires 5V for the logic and +/- 12 VDC for supporting the add on boards. The +/- 12 V supplies are routed directly to the six headers.

Anyway, it is all a lot of fun!

73 de Phil N8VB

<http://www.philcovington.com/SDR.html>

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From: "nalaregeork" <nospam@...> [nalaregeork](#)

Date: Fri Oct 7, 2005 2:53 pm [Offline](#)

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I just got my Softrock kit and other then building it and using it as suggested I was wondering about all the other possibilities. I saw one config where they used a DDS card and an AVR Butterfly to control the Softrock. I also have had a desire to try a Gumstix unit for other reasons and thought this might be one of the possible applications. Oh well lots of possibilities and likely I will start with the Softrock and PowerSDR under Windows XP. I do have a Debian box I could try out as the next iteration provided I can figure out how to make the Softrock work under a linux environment ;D

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Alan K
KC2ORW

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Author

Date

6 [Re: Hello all](#)

Hi Alan, When I get the time I want to update my SharpDSP library and create a SoftRock40 specific console. I have already had success using the SharpDSP...

Phil Covington
[phil_at_vhmi](#) Oct 7, 2005
 3:19 pm

7 [Re: Hello all](#)

Awsome Phil I guess I will have to get my unit built and then get my Mono config working on the Sarge box. Pretty sure Mono was compiling the samples under...

[nalaregeork](#) Oct 7, 2005
 3:26 pm

8 [Re: Hello all](#)

Hmm I was reading some of the materials on your pages and saw you may next be looking to get the Windows software packaged as MSI's and I have done authoring...

[nalaregeork](#) Oct 7, 2005
 6:14 pm

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From: "Phil Covington" <phil@...> [phil at vhmi](#)

Date: Fri Oct 7, 2005 3:19 pm [Offline](#)

● Polls Subject: Re: Hello all

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Hi Alan,

● Promote

When I get the time I want to update my SharpDSP library and create a SoftRock40 specific console. I have already had success using the SharpDSP library in Linux under the Mono framework. The console app will be targeted to the Mono framework so it will run in both Windows and Linux without having to recompile. Right now Debian is my favorite distro so I am doing some development work on it.

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73 de Phil N8VB

Did you know...

--- In [Open High Performance SDR@yahoogroups.com](#), "nalaregeork"

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messages? Just

> I just got my Softrock kit and other then building it and using it as
> suggested I was wondering about all the other possibilities. I saw one
> config where they used a DDS card and an AVR Butterfly to control the
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> Later On

> Alan K

> KC2ORW

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  6:14 pm

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From: "nalaregeork" <nospam@...> [nalaregeork](#)
 Date: Fri Oct 7, 2005 3:26 pm [Offline](#)
 Subject: Re: Hello all [Send Email](#)

Awesome Phil I guess I will have to get my unit built and then get my Mono config working on the Sarge box. Pretty sure Mono was compiling the samples under Debian for me but, didn't do much testing beyond compiling the samples. So, perhaps I can contribute some labor or at the least testing in the near future.

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73 A1 KC2ORW

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "Phil Covington" <phil@p...> wrote:

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> When I get the time I want to update my SharpDSP library and create a SoftRock40 specific console. I have already had success using the SharpDSP library in Linux under the Mono framework. The console app will be targeted to the Mono framework so it will run in both

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● [Database](#) *From:* "nalaregeork" <nospam@...> [nalaregeork](#)
 ● *Date:* Fri Oct 7, 2005 6:12 pm [Offline](#)
 ● *Subject:* Re: Hello all [Send Email](#)

Hmm I was reading some of the materials on your pages and saw you may next be looking to get the Windows software packaged as MSI's and I have done authoring of MSI packages professionally no less. If you ever want to get that going let me know I still have Wise 4.2 available too me.

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73 de Al KC2ORW
Echolink via WB2IXR-R

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Did you know...

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From: "nalaregeork" <nospam@...> [nalaregeork](#)

Date: Sun Oct 9, 2005 6:32 pm [Offline](#)

Subject: Soldering SMT Components [Send Email](#)

Yikes them chips is small.....

Got to about halfway through the soldering of the surface mount components on my SoreEyes40 (aka SoftRock40) only have to laydown the surface mount resistors (they look like a bigger pain then the IC's) Oh well anyone else try soldering surface mount besides Phil?

I have a headband visor with two different magnifications even then this stuff looks small and requires tricks to keep the hands steady....

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• Database *From: "Phil Covington" <phil@...>*

[phil at vhma](#)

• Polls *Date: Tue Oct 11, 2005 11:47 pm*

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• Members

Subject: SharpDSP Mini Console for the SoftRock 40 now available...

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Get it at <http://www.philcovington.com/SDR.html>

• Promote

Run the program... Click on Setup and make the appropriate changes for your sound card (Source/Sink), select your desired sampling rate and block size, and then Click on Start...

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I/Q balance can be adjusted manually as well as s-meter level correction on the Levels->Correction menu.

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If you are using a different crystal you can enter the center frequency on the Setup->Center Freq menu. It defaults to 7.056 Mhz

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Note that the DSB/LSB/USB/CW modes are combined into one selection right now. This is because the only difference between those modes is the filter values. The high and low filter values can be set between +/- 8000 Hz individually so you can tune USB - LSB - DSB - CWL - CWU - ISB - etc.. Other modes are AM, SAM, FM, and SPEC. I will add the individual SSB/DSB modes in the next release since it only involves shifting the filters around.

Configuration settings are saved in a file called softrock.config.xml in the application's folder. It is an XML file so you can edit/view it with a text editor or web browser if desired.

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If you changed the sampling rate or block size you need to click the Stop button and then the Start button for the changes to take effect.

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The code was written with the open source SharpDevelop IDE. I hope that this will get people interested in writing their own code. It is

through blogs, also meant as a demonstration of how to use the SharpDSP library. photos and more. [Create your own 360° page now.](#) This is a pre-Alpha release so expect some bugs. This preliminary release supports:

- * Sample rates up to 96000 Hz
- * Selectable block sizes from 64 to 8192
- * Settings are saved in a text editable XML file (softrock.config.xml)
- * Center frequency can be set for other bands besides 40 meters.
- * Very low resource requirements (should help on older machines)

Future updates will include the following features:

- * Dual receive capability within the Rx passband
- * Analog S-meter
- * Panadapter Display
- * TCP CAT support
- * Serial CAT support
- * TCP Audio Server support
- * Much more...

report problems/bugs to Phil Covington, N8VB at p.covington@...

73 de Phil N8VB

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[phil_at_vhmi](#)

● Database *From:* "Phil Covington" <phil@...>

[Offline](#)

Date: Wed Oct 12, 2005 2:08 am

● Polls *Subject:* SharpDSP Mini Console for the SoftRock 40 bugfix #1... [Send Email](#)

● Members Thanks to Bruce N1RX, the first bug in the SharpDSP Mini Console for

● Calendar the SoftRock 40 has been corrected. The updated binary and source are

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Author

Date

[Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#)

[nalaregeork](#)

Oct 12, 2005 6:53 pm

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[Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#)

[nalaregeork](#)

Oct 12, 2005 7:13 pm

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[kd5nwa](#)

Oct 12, 2005 8:38 pm

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[Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#)



[nalaregeork](#)



Oct 12, 2005 8:52 pm



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From: "observer35" <rein0zn@...>

[observer35](#)

Date: Wed Oct 12, 2005 5:56 pm

[Offline](#)

Subject: Required computer platform(s) for SharpDSP

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Hello All,

Just new to the group.

I am trying to get some idea about the required platform. First there is Linux as I understand it. Then Windows. XT/2000/NT ? What about win98/98SE?

Perhaps I am just overlooking this. In that case, I am sorry.

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 73 Rein W6/PA0ZN

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Date

[Re: Required computer platform\(s\) for SharpDSP](#)

Philip Covington

13 Hi Rein, I am not sure if you are referring to the PowerSDR Console, the SharpDSP SoftRock Mini Console, or future software for the HPSDR project. PowerSDR...

[phil_at_vhmi](#)

Oct 12, 2005 6:16 pm



[Re: Required computer platform\(s\) for SharpDSP](#)

Rein A. Smit

16 Hello Phil, Thanks. I was referring to the HPSDR. Fine that it will be running on Linux in the future. In reference to Linrad, you might want to look into the...

[observer35](#)

Oct 12, 2005 8:06 pm



[Re: Required computer platform\(s\) for SharpDSP](#)

Philip Covington

17 Hi Rein, The HPSDR will have its own A/Ds and D/As on board so there will be no need for the sound card for the IQ data in/out. The IQ data will come over...

[phil_at_vhmi](#)

Oct 12, 2005 8:22 pm



- 29 [Re: Required computer platform\(s\) for SharpDSP](#) Rein A. Smit [observer35](#) Oct 12, 2005
Hi Phil, Got it. Do you have any idea how much the additional hardware will cost.? Are you planning on selling this(these) boards? I see that I have to read... 9:54 pm
- 31 [Re: Required computer platform\(s\) for SharpDSP](#) Philip Covington [phil_at_vhmi](#) Oct 12, 2005
Hi Rein, The HPSDR is an open source/open hardware project so all design files will be available for download. I do plan to offer boards from bare to fully ... 10:05 pm
- 19 [Re: Required computer platform\(s\) for SharpDSP](#) [nalaregeork](#) Oct 12, 2005
Try the Envy24 driver supposed to work with Delta 44 and Audiphile 2496 <http://www.opensound.com/readme/README.Envy24.html> I was eventually going to try it on... 8:44 pm

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[phil at vhmi](#)

● Database *From:* Philip Covington <p.covington@...>

 [Offline](#)

Date: Wed Oct 12, 2005 6:16 pm

● Polls *Subject:* Re: [Open_HPSSDR] Required computer platform(s) for SharpDSP  [Send Email](#)

● Members

● Calendar Hi Rein,

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I am not sure if you are referring to the PowerSDR Console, the SharpDSP SoftRock Mini Console, or future software for the HPSDR project.

PowerSDR Console:

Runs on Windows 2000 or XP. No Linux.

SharpDSP SoftRock 40 Mini Console:

Runs on Windows 2000 or XP. Soon to run on Linux.

HPSDR:

Windows 2k/XP and Linux planned.

There is no planned support for Windows 95/98/98SE/ME. There is too many problems supporting the Win 9X series.

73 de Phil N8VB

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>
>
>
>
>
> Build: 8 succeeded, 0 failed, 0 skipped
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Philip A Covington
<http://www.philcovington.com>

Replies

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|----|---|--|--------------------------|
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Oct 12, 2005

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● Database *From:* "nalaregeork" <nospam@...>

[nalaregeork](#)

● Polls *Date:* Wed Oct 12, 2005 6:51 pm

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● Members *Subject:* Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... [Send Email](#)

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73 de KC2ORW Al

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From: "nalaregeork" <nospam@...>

[nalaregeork](#)

Date: Wed Oct 12, 2005 7:13 pm

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Subject: Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

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[observer35](#)

● Database *From: "Rein A. Smit" <rein0zn@...>*

[Offline](#)

● Polls *Date: Wed Oct 12, 2005 8:05 pm*

● Members *Subject: Re: [Open_HPSPDR] Required computer platform(s) for SharpDSP* [Send Email](#)

● Calendar Hello Phil,

● Promote Thanks. I was referring to the HPSDR.

? Already a member? [Sign in to Yahoo!](#) Fine that it will be running on Linux in the future.

In reference to Linrad, you might want to look into the sound cards/alsaOSS area.

Yahoo! Groups Tips That is, which cards are going to be supported and which not. There are no Linux drivers for the delta44 card..

Did you know...

You can search 73 Rein W6/PA0ZN

the group for older [messages](#). Philip Covington wrote:

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>Hi Rein,

Keep connected >
to your friends >I am not sure if you are referring to the PowerSDR Console, the
and family >SharpDSP SoftRock Mini Console, or future software for the HPSDR
through blogs, >project.

photos and >
more. [Create](#) >PowerSDR Console:

[your own 360°](#) >
[page now.](#) >Runs on Windows 2000 or XP. No Linux.

>SharpDSP SoftRock 40 Mini Console:

>
>Runs on Windows 2000 or XP. Soon to run on Linux.

>

>HPSSDR:

>

>Windows 2k/XP and Linux planned.

>

>There is no planned support for Windows 95/98/98SE/ME. There is too
>many problems supporting the Win 9X series.

>

>73 de Phil N8VB

>

>

>

>On 10/12/05, observer35 <rein0zn@...> wrote:

>

>

>> Hello All,

>>

>> Just new to the group.

>> I am trying to get some idea about the required platform.

>> First there is Linux as I understand it. Then Windows.

>> XT/2000/NT ? What about win98/98SE?

>>

>> Perhaps I am just overlooking this. In that case, I am sorry.

>>

>> 73 Rein W6/PA0ZN

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>> Build: 8 succeeded, 0 failed, 0 skipped

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>Philip A Covington









><http://www.philcovington.com>

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>Build: 8 succeeded, 0 failed, 0 skipped
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| | Replies | Author | Date |
|----|---|---|--------------------------|
| 17 | Re: Required computer platform(s) for SharpDSP Hi Rein, The HPSPDR will have its own A/Ds and D/As on board so there will be no need for the sound card for the IQ data in/out. The IQ data will come over... | Philip Covington phil_at_vhmi   | Oct 12, 2005 8:22 pm |
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| 19 | Re: Required computer platform(s) for SharpDSP Try the Envy24 driver supposed to work with Delta 44 and Audiphile 2496 http://www.opensound.com/readme/README.Envy24.html I was eventually going to try it on... | nalaregeork   | Oct 12, 2005 8:44 pm |

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● Database *From:* Philip Covington <p.covington@...>

[phil at vhm](#)

● Polls *Date:* Wed Oct 12, 2005 8:22 pm

[Offline](#)

● Members

● Calendar *Subject:* Re: [Open_HPSSDR] Required computer platform(s) for SharpDSP [Send Email](#)

● Promote Hi Rein,

The HPSDR will have its own A/Ds and D/As on board so there will be no need for the sound card for the IQ data in/out. The IQ data will come over USB 2.0 via libusb(-win32) on both Windows and Linux. That was one of my main motivations in starting the HPSDR project - to get away from sound cards.

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Yahoo! Groups Tips

73 de Phil N8VB

Did you know... On 10/12/05, Rein A. Smit <[rein0zn@...](#)> wrote:

> Hello Phil,

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> Thanks. I was referring to the HPSDR.

sort order of >

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more. [Create](#) > >project.
[your own 360°](#) > >
[page now.](#) > >PowerSDR Console:
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> >> Build: 8 succeeded, 0 failed, 0 skipped
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> >> YAHOO! GROUPS LINKS
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> >Philip A Covington
> >http://www.philcovington.com
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Philip A Covington
<http://www.philcovington.com>

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| | | Author | Date |
|----|--|---|--------------------------|
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From: KD5NWA <KD5NWA@...>

[kd5nwa](#)

Date: Wed Oct 12, 2005 8:38 pm

[Offline](#)

Subject: Re: [Open_HPSSDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

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I tried during my lunch break to setup SharpDevelop and Phil's new software. It would compile without errors but it would get an un-handled exception error and abort when executed.

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Disclaimer;

This is the first time I have used C# and SharpDevelop to compile anything, so saying I'm un-experienced is a understatement.

Yahoo! Groups Tips

At 01:51 PM 10/12/2005, nalaregeork wrote:

Did you know...

```

>Hee Hee I suppose I should finish my hardware first before compiling
>and trying to use the software. I did a quick export of the project to
>VS 2003 and then updated the references to get it to function. I had to
>comment out audio.InitializeAudio() call to get it to run for some
>reason or other. Hmm would of thought initializing audio wouldn't have
>been a big deal I have an M-Audio Audiophile installed or is there
>something in the code that requires a Delta 44?
>Oh well will examine that a little more later as this was just a quick
>export and compile.

```

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>Well any old way looks pretty good Phil

>73 de KC2ORW A1

>I better be quick I think there going to shut down the power shortly due to the flooding....

>

>--- In [Open High Performance SDR@yahoogroups.com](#), "Phil Covington"

><phil@p...> wrote:













> >







> > Thanks to Bruce N1RX, the first bug in the SharpDSP Mini Console for the SoftRock 40 has been corrected. The updated binary and source are on my webpage.

```
> >
> > 73 de Phil N8VB
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>Build: 8 succeeded, 0 failed, 0 skipped
>Yahoo! Groups Links
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```

Cecil Bayona
 KD5NWA
 www.qrpradio.com

I fail to see why doing the same thing over and over and getting the same results every time is insanity: I've almost proved it isn't; only a few more tests now and I'm sure results will differ this time ...

| | Replies | Author | Date |
|----|---|--|--|
| 20 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... I figured it out it does compile fine and then it has that error on the audio.InitializeAudio function. I commented that out //audio.InitializeAudio(); Then it...</p> | <p>nalaregeork  </p> | <p>Oct 12, 2005 8:52 pm</p> |
| 21 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Hi all, You need to have the PA19.dll located somewhere in your path or (better) in the folder with the SharpDSP console's executable. I also just released a...</p> | <p>Philip Covington phil_at_vhmi  </p> | <p>Oct 12, 2005 9:15 pm</p> |
| 22 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Yeah I fixed that one on my last go round and figured out the blank config issue at the same time that relates to the error with audio.InitializeAudio(); Later...</p> | <p>nalaregeork  </p> | <p>Oct 12, 2005 9:18 pm</p> |
| 23 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Thanks, I got to the setup screen but the source/sink fields had no choices to pick from. It might be related to the way I have it setup. After unzipping the ...</p> | <p>kd5nwa  </p> | <p>Oct 12, 2005 9:22 pm</p> |
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| 25 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Oops! ... The other thing that can cause this is that you are using the softrock.config.xml file that came with the code. The settings for the sound card is...</p> | <p>Philip Covington phil_at_vhmi  </p> | <p>Oct 12, 2005 9:35 pm</p> |

- 26 [Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#) [nalaregeork](#) Oct 12, 2005
Oh well not too bad for an early version of a software package Later Al
KC2ORW ... Source/Sink ... If ... will...   9:44 pm
- 27... [Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#) [nalaregeork](#) Oct 12, 2005
Great stuff over at <http://www.codeproject.com/> but most of it is Visual
Studio 2002/2003 and not much of it geared towards new users of
SharpDevelop....   9:46 pm
- 28 [Question 1\) Out of sequence postings? and Question 2\) Boards?](#) [nalaregeork](#) Oct 12, 2005
Posting order is whacked I sent two replies in realtive proximity and the first
reply showed up after the second reply oh well this is Yahoo and it is free ;D ...   9:53 pm

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[nalaregeork](#)

● Database *From:* "nalaregeork" <nosпам@...>

[Offline](#)

Date: Wed Oct 12, 2005 8:44 pm

● Polls *Subject:* Re: [Open_HPSSDR] Required computer platform(s) for SharpDSP [Send Email](#)

● Members Try the Envy24 driver supposed to work with Delta 44 and Audiphile 2496

● Calendar <http://www.opensound.com/readme/README.Envy24.html>

● Promote I was eventually going to try it on my Debian box when I move the spare Audiophile over to it....

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "Rein A. Smit"

<rein0zn@i...> wrote:

>

> Hello Phil,

>

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From: "nalaregeork" <nospam@...>

Date: Wed Oct 12, 2005 8:52 pm

Subject: [Open_HPSPDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

[nalaregeork](#)



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I figured it out it does compile fine and then it has that error on the audio.InitializeAudio function. I commented that
out //audio.InitializeAudio();

Then it ran but I still had an error it was then that I figured out how I was supposed to use the software. You have to setup your card via the menu "Setup" "Source/Sink" Once you do that the program write to the softrock.config.xml file and you can remove the comment from audio.InitializeAudio(); function and will get no more errors. I guess what it needs is to have a check for an empty configuration to avoid seeing the error or even present you with a message to use "Source/Sink" to setup the card.

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| 30 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...</p> <p>I installed the beta personal version of VS on my PC, the performance was horrible, but with SharpDevelop my PC was responsive and I had no problems zipping...</p> | kd5nwa | Oct 12, 2005 9:55 pm |

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• Database *From:* Philip Covington <p.covington@...>

[phil at vhma](#)

Date: Wed Oct 12, 2005 9:15 pm

[Offline](#)

• Polls *Subject:* Re: [Open_HP_SDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... [Send Email](#)

• Members

• Calendar Hi all,

• Promote You need to have the PA19.dll located somewhere in your path or (better) in the folder with the SharpDSP console's executable.

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I also just released a new version (many changes and bugfixes) today at:

<http://www.philcovington.com/SDR.html>

Yahoo! Groups Tips

Please read the entry for 10/12/2005 in my blog at

Did you know... <http://pcovington.blogspot.com/> It describes the calibration procedures.

You can search the group for 73 de Phil N8VB older [messages](#).

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On 10/12/05, nalaregeork <nospam@...> wrote:

```

> I figured it out it does compile fine and then it has that error on
> the audio.InitializeAudio function. I commented that
> out //audio.InitializeAudio();
> Then it ran but I still had an error it was then that I figured out
> how I was supposed to use the software. You have to setup your card
> via the menu "Setup" "Source/Sink" Once you do that the program write
> to the softrock.config.xml file and you can remove the comment from
> audio.InitializeAudio(); function and will get no more errors. I
> guess what it needs is to have a check for an empty configuration to
> avoid seeing the error or even present you with a message to
> use "Source/Sink" to setup the card.
>
>
> --- In Open\_High\_Performance\_SDR@yahoogroups.com, KD5NWA

```



```



> >
> > Cecil Bayona
> > KD5NWA
> > www.qrpradio.com
> >
> > I fail to see why doing the same thing over and over and getting
> the
> > same results every time is insanity: I've almost proved it isn't;
> > only a few more tests now and I'm sure results will differ this
> time ...
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>
>
>
>
> Build: 8 succeeded, 0 failed, 0 skipped
> Yahoo! Groups Links
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```

--
Philip A Covington
<http://www.philcovington.com>

Replies

Author Date

22 [Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#) [nalaregeork](#) Oct 12, 2005
Yeah I fixed that one on my last go round and figured out the blank config issue at the   9:18 pm
same time that relates to the error with audio.InitializeAudio(); Later...

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From: "nalaregeork" <nospam@...>

Date: Wed Oct 12, 2005 9:18 pm

Subject: [Open_HP_SDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

[nalaregeork](#)

[Offline](#)

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Yeah I fixed that one on my last go round and figured out the blank config issue at the same time that relates to the error with `audio.InitializeAudio();`

? Already a member? [Sign in to Yahoo!](#) Later on looks good Phil
73 de KC2ORW Al

Yahoo! Groups Tips --- In [Open_High_Performance_SDR@yahoogroups.com](#), Philip Covington <p.covington@g...> wrote:

Did you know... > Hi all,

You can search the group for older [messages](#). > You need to have the PA19.dll located somewhere in your path or (better) in the folder with the SharpDSP console's executable.

Yahoo! 360° > I also just released a new version (many changes and bugfixes) today at:

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> Please read the entry for 10/12/2005 in my blog at <http://pcovington.blogspot.com/> It describes the calibration procedures.

> 73 de Phil N8VB

> On 10/12/05, nalaregeork <nospam@a...> wrote:

> > I figured it out it does compile fine and then it has that error on

> > the `audio.InitializeAudio` function. I commented that


```
> > out //audio.InitializeAudio();
> > Then it ran but I still had an error it was then that I figured
out
> > how I was supposed to use the software. You have to setup your
card
> > via the menu "Setup" "Source/Sink" Once you do that the program
write
> > to the softrock.config.xml file and you can remove the comment
from
> > audio.InitializeAudio(); function and will get no more errors. I
> > guess what it needs is to have a check for an empty configuration
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> > avoid seeing the error or even present you with a message to
> > use "Source/Sink" to setup the card.
> >
> >
> > --- In Open\_High\_Performance\_SDR@yahoo.com, KD5NWA
> > <KD5NWA@c...> wrote:
> > >
> > > I tried during my lunch break to setup SharpDevelop and Phil's
new
> > > software. It would compile without errors but it would get an
> > > un-handled exception error and abort when executed.
> > >
> > > Disclaimer;
> > >
> > > This is the first time I have used C# and SharpDevelop to
compile
> > > anything, so saying I'm un-experienced is a understatement.
> > >
> > > At 01:51 PM 10/12/2005, nalaregeork wrote:
> > > >Hee Hee I suppose I should finish my hardware first before
> > > compiling
> > > >and trying to use the software. I did a quick export of the
> > > project to
> > > >VS 2003 and then updated the references to get it to function.
I
> > > had to
> > > >comment out audio.InitializeAudio() call to get it to run for
some
> > > >reason or other. Hmm would of thought initializing audio
wouldn't
> > > >have
> > > >>been a big deal I have an M-Audio Audiophile installed or is
there
> > > >>something in the code that requires a Delta 44?
> > > >>Oh well will examine that a little more later as this was just
a
> > > >>quick
> > > >>export and compile.
> > > >>Well any old way looks pretty good Phil
> > > >>73 de KC2ORW Al
> > > >>I better be quick I think there going to shut down the power
> > > >>shortly
> > > >>due to the flooding....
> > > >>
```

```
> > > >--- In Open\_High\_Performance\_SDR@yahoo.com, "Phil  
Covington"  
> > > ><phil@p...> wrote:  
> > > >  
> > > > Thanks to Bruce N1RX, the first bug in the SharpDSP Mini  
> > Console for  
> > > > the SoftRock 40 has been corrected. The updated binary and  
> > source are  
> > > > on my webpage.  
> > > >  
> > > > 73 de Phil N8VB  
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> > > >  
> > > >Build: 8 succeeded, 0 failed, 0 skipped  
> > > >Yahoo! Groups Links  
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> > > >  
> > > >  
> > >  
> > > Cecil Bayona  
> > > KD5NWA  
> > > www.qrpradio.com  
> > >  
> > > I fail to see why doing the same thing over and over and getting  
> > the  
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isn't;  
> > > only a few more tests now and I'm sure results will differ this  
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> --
> Philip A Covington
> <http://www.philcovington.com>
>

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From: KD5NWA <KD5NWA@...>

Date: Wed Oct 12, 2005 9:23 pm

Subject: Re: [Open_HP_SDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

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Thanks, I got to the setup screen but the source/sink fields had no choices to pick from.

It might be related to the way I have it setup. After unzipping the files to a project directory, I tried compiling it and it complained that it could not find the DLL files. The place it was looking did not exist, so I created the folders and put a copy of the "bin" directories there. After that it compiles with one warning;

d:\Data\My Documents\Visual Studio 2005\Projects\SoftRock_SharpDSP_Console\MainForm.cs(401,29): warning CS0649: Field 'SharpDSP_Console.MainForm.rx_sub' is never assigned to, and will always have its default value null

Yahoo! Groups Tips

I will setup my PC at home with the software and see what I can find, it's tough when you don't have a clue. This weekend it's off to the bookstore to find some books on .NET, and find some tutorials on SharpDevelop on the Internet.

Did you know...

You can set the sort order of messages? Just click on the link in the **date** column. Your preferences will be remembered, so you don't have to do it again when you return.

At 03:52 PM 10/12/2005, you wrote:

I figured it out it does compile fine and then it has that error on the audio.InitializeAudio function. I commented that out //audio.InitializeAudio();
Then it ran but I still had an error it was then that I figured out how I was supposed to use the software. You have to setup your card via the menu "Setup" "Source/Sink" Once you do that the program write to the softrock.config.xml file and you can remove the comment from audio.InitializeAudio(); function and will get no more errors. I guess what it needs is to have a check for an empty configuration to avoid seeing the error or even present you with a message to use "Source/Sink" to setup the card.

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--- In Open_High_Performance_SDR@yahoogroups.com, KD5NWA

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<KD5NWA@c...> wrote:
>
> I tried during my lunch break to setup SharpDevelop and Phil's new
> software. It would compile without errors but it would get an
> un-handled exception error and abort when executed.
>
> Disclaimer;
>
> This is the first time I have used C# and SharpDevelop to compile
> anything, so saying I'm un-experienced is a understatement.
>

Cecil Bayona
KD5NWA
www.qrpradio.com

I fail to see why doing the same thing over and over and getting the same results every time is insanity: I've almost proved it isn't; only a few more tests now and I'm sure results will differ this time ...

| | Replies | Author | Date |
|----|---|--|----------------------------------|
| 24 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Hi Cecil, The warning is ok. It just tells you that you declared a variable or object but did not later use it or assign to it. This is a place holder for...</p> | <p>Philip Covington phil_at_vhmi  </p> | <p>Oct 12, 2005 9:29 pm</p> |
| 25 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Ooops! ... The other thing that can cause this is that you are using the softrock.config.xml file that came with the code. The settings for the sound card is...</p> | <p>Philip Covington phil_at_vhmi  </p> | <p>Oct 12, 2005 9:35 pm</p> |
| 26 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Oh well not too bad for an early version of a software package Later Al KC2ORW ... Source/Sink ... If ... will...</p> | <p>nalaregeork  </p> | <p>Oct 12, 2005 9:44 pm</p> |
| 27 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... ... Great stuff over at http://www.codeproject.com/ but most of it is Visual Studio 2002/2003 and not much of it geared towards new users of SharpDevelop....</p> | <p>nalaregeork  </p> | <p>Oct 12, 2005 9:46 pm</p> |
| 28 | <p>Question 1) Out of sequence postings? and Question 2) Boards? Posting order is whacked I sent two replies in realtive proximity and the first reply showed up after the second reply oh well this is Yahoo and it is free ;D ...</p> | <p>nalaregeork  </p> | <p>Oct 12, 2005 9:53 pm</p> |
| 30 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... I installed the beta personal version of VS on my PC, the performance was horrible, but with SharpDevelop my PC was responsive and I had no problems zipping...</p> | <p>kd5nwa  </p> | <p>Oct 12, 2005 9:55 pm</p> |
| 32 | <p>Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... Well regardless of your choice of dev. env. my point was that you can learn a lot of C# from Codeproject because I just tested the import feature of...</p> | <p>nalaregeork  </p> | <p>Oct 12, 2005 10:36 pm</p> |

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• Links *From:* Philip Covington <p.covington@...>

[phil_at_vhmi](#)

Date: Wed Oct 12, 2005 9:29 pm

[Offline](#)

Subject: Re: [Open_HPSPDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... [Send Email](#)

• Polls

• Members Hi Cecil,

• Calendar The warning is ok. It just tells you that you declared a variable or
• Promote object but did not later use it or assign to it. This is a place
holder for the sub-receiver that I will add in a future update. The
new version on my webpage will not give this error since I declared
the object and assigned null to it to get rid of the warning.

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Yahoo! Groups Tips

If you do not see any drivers or sound cards listed in the Source/Sink window then it is probably because you do not have the PA19.dll somewhere where the program can find it.

Did you know...

The PA19.dll is included in the binary distribution and resides in the application's folder.

You can set the sort order of messages? Just 73 de Phil N8VB

click on the **date** On 10/12/05, KD5NWA <KD5NWA@...> wrote:

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again when you > not find the DLL files. The place it was looking did not exist, so I created
return. > the folders and put a copy of the "bin" directories there. After that it
> compiles with one warning;

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Keep connected > d:\Data\My Documents\Visual Studio
to your friends > 2005\Projects\SoftRock_SharpDSP_Console\MainForm.cs(401,29):
and family > warning CS0649: Field 'SharpDSP_Console.MainForm.rx_sub' is
through blogs, > never assigned to, and will always have its default value null
photos and > I will setup my PC at home with the software and see what I can find, it's

more. [Create your own 360° page now.](#) > tough when you don't have a clue. This weekend it's off to the bookstore to
> find some books on .NET, and find some tutorials on SharpDevelop on the
> Internet.
>
> By the way does anyone know if this list will allow emails that are not
> plain text?
>
>
> At 03:52 PM 10/12/2005, you wrote:
>
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>
>
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> >
> Cecil Bayona
> KD5NWA
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> Build: 8 succeeded, 0 failed, 0 skipped
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>
>
> _____
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--

Philip A Covington
<http://www.philcovington.com>

| | Replies | Author | Date |
|----|--|---|-------------------------|
| | Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... | Philip Covington phil_at_vhmi | Oct 12, 2005 9:35 pm |
| 25 | Ooops! ... The other thing that can cause this is that you are using the softrock.config.xml file that came with the code. The settings for the sound card is... |   | |
| 26 | Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1... | nalaregeork | Oct 12, 2005 9:44 pm |
| | Oh well not too bad for an early version of a software package Later AI KC2ORW ... Source/Sink ... If ... will... |   | |

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From: Philip Covington <p.covington@...>

Date: Wed Oct 12, 2005 9:35 pm

Subject: Re: [Open_HP_SDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

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Oops!

> If you do not see any drivers or sound cards listed in the Source/Sink
> window then it is probably because you do not have the PA19.dll
> somewhere where the program can find it.

The other thing that can cause this is that you are using the softrock.config.xml file that came with the code. The settings for the sound card is probably not valid on your system. You need to select the sound card and then when you exit the program it will be saved to the softrock.config.xml file.

In the new version that I released today, I do a check to see if you are using a softrock.config.xml file from an earlier installation. If so the program will delete it and then recreate it.

It is also safe to delete the softrock.config.xml file. On startup the program will create it if it does not exist. Of course, you will have to set everything up again though.

73 de Phil N8VB

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Replies

Author **Date**

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[Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#)

[nalaregeork](#) Oct 12, 2005

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From: "nalaregeork" <nospam@...>

Date: Wed Oct 12, 2005 9:44 pm

Subject: [Open_HPSPDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

[nalaregeork](#)

[Offline](#)

[Send Email](#)

Oh well not too bad for an early version of a software package

Later Al KC2ORW

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--- In [Open High Performance SDR@yahoogroups.com](#), Philip Covington <p.covington@g...> wrote:

>
> Ooops!

Yahoo! Groups Tips

>
>

Did you know...

> > If you do not see any drivers or sound cards listed in the Source/Sink

You can search the group for older [messages](#).

> > window then it is probably because you do not have the PA19.dll somewhere where the program can find it.

Yahoo! 360°

> The other thing that can cause this is that you are using the softrock.config.xml file that came with the code. The settings for the sound card is probably not valid on your system. You need to select the sound card and then when you exit the program it will be saved to the softrock.config.xml file.

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> In the new version that I released today, I do a check to see if you are using a softrock.config.xml file from an earlier installation.

If so the program will delete it and then recreate it.

> It is also safe to delete the softrock.config.xml file. On startup the program will create it if it does not exist. Of course, you will have to set everything up again though.

>
> 73 de Phil N8VB
>

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• Database *From: "nalaregeork" <nospam@...>*

[nalaregeork](#)

• Polls *Date: Wed Oct 12, 2005 9:41 pm*

[Offline](#)

• Members *Subject: [Open_HPSPDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...*

[Send Email](#)

• Calendar *--- In [Open_High_Performance_SDR@yahoogroups.com](#), KD5NWA <KD5NWA@c...>*

• Promote *wrote:*

> it's tough when you don't have a clue. This weekend it's off to the
> bookstore to find some books on .NET, and find some tutorials on
> SharpDevelop on the Internet.

? Already a member? [Sign in to Yahoo!](#)

Yahoo! Groups Tips

Did you know...

You can set the sort order of messages? Just click on the link in the **date** column. Your

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preferences will be remembered, so you don't have to do it again when you return.

> By the way does anyone know if this list will allow emails that are
> not plain text?

yeah I think it will????

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- 28 [Question 1\) Out of sequence postings? and Question 2\) Boards?](#) [nalaregeork](#) Oct 12, 2005
Posting order is whacked I sent two replies in realtive proximity and the first reply showed up after the second reply oh well this is Yahoo and it is free ;D ...   9:53 pm
- 30 [Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#) [kd5nwa](#) Oct 12, 2005
I installed the beta personal version of VS on my PC, the performance was horrible, but with SharpDevelop my PC was responsive and I had no problems zipping...   9:55 pm
- 32 [Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#) [nalaregeork](#) Oct 12, 2005
Well regardless of your choice of dev. env. my point was that you can learn a lot of C# from Codeproject because I just tested the import feature of...   10:36 pm

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• Database *From: "nalaregeork" <nospam@...>*

[nalaregeork](#)

• Polls *Date: Wed Oct 12, 2005 9:53 pm*

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Subject: Question 1) Out of sequence postings? and Question 2) Boards?

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Posting order is whacked I sent two replies in realtive proximity and the first reply showed up after the second reply oh well this is Yahoo and it is free ;D

• Promote

If you get enough interest perhaps setup a forum which are nice for rereading old useful posts.

? Already a member? [Sign in to Yahoo!](#)

Oh yeah so, when you going to release the boards you are creating Phil?

Yahoo! Groups Tips

--- In [Open High Performance SDR@yahoogroups.com](#), "nalaregeork"

Did you know... <nospam@a...> wrote:
>

You can set the > --- In [Open High Performance SDR@yahoogroups.com](#), KD5NWA
sort order of <KD5NWA@c...>

messages? Just > wrote:

click on the > > it's tough when you don't have a clue. This weekend it's off to
link in the **date** the

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so you don't >

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[your own 360°](#) > > By the way does anyone know if this list will allow emails that
[page now.](#) are
> > not plain text?
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> yeah I think it will????
>

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[observer35](#)

● Database *From: "Rein A. Smit" <rein0zn@...>*

[Offline](#)

Date: Wed Oct 12, 2005 9:53 pm

● Polls *Subject: Re: [Open_HPSSDR] Required computer platform(s) for SharpDSP* [Send Email](#)

● Members

● Calendar Hi Phil,

● Promote

Got it. Do you have any idea how much the additional hardware will cost.?

Are you planning on selling this(these) boards?

I see that I have to read up on C# .

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Yahoo! Groups Tips 73 Rein W6/PA0ZN

Did you know...

You can search Philip Covington wrote:
the group for

older [messages](#). >Hi Rein,

>

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>The HPSDR will have its own A/Ds and D/As on board so there will be no

>need for the sound card for the IQ data in/out. The IQ data will

Keep connected

>come over USB 2.0 via libusb(-win32) on both Windows and Linux. That

to your friends

>was one of my main motivations in starting the HPSDR project - to get

and family

>away from sound cards.

through blogs,

>

photos and

>73 de Phil N8VB

more. [Create](#)

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>On 10/12/05, Rein A. Smit <[rein0zn@...](#)> wrote:

[page now.](#)

>

>

>>Hello Phil,

>>

>>Thanks. I was referring to the HPSDR.

>>
>>Fine that it will be running on Linux in the future.
>>
>> In reference to Linrad, you might want to look into the sound
>>cards/alsaOSS area.
>> That is, which cards are going to be supported and which not. There are
>>no Linux
>> drivers for the delta44 card..
>>
>>73 Rein W6/PA0ZN
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>>
>>Philip Covington wrote:
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>>>Hi Rein,
>>>
>>>I am not sure if you are referring to the PowerSDR Console, the
>>>SharpDSP SoftRock Mini Console, or future software for the HPSDR
>>>project.
>>>
>>>PowerSDR Console:
>>>
>>>Runs on Windows 2000 or XP. No Linux.
>>>
>>>SharpDSP SoftRock 40 Mini Console:
>>>
>>>Runs on Windows 2000 or XP. Soon to run on Linux.
>>>
>>>HPSDR:
>>>
>>>Windows 2k/XP and Linux planned.
>>>
>>>There is no planned support for Windows 95/98/98SE/ME. There is too
>>>many problems supporting the Win 9X series.
>>>
>>>73 de Phil N8VB
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>>>On 10/12/05, observer35 <rein0zn@...> wrote:
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>>>>Hello All,
>>>>
>>>>Just new to the group.
>>>>I am trying to get some idea about the required platform.
>>>>First there is Linux as I understand it. Then Windows.
>>>>XT/2000/NT ? What about win98/98SE?
>>>>
>>>>Perhaps I am just overlooking this. In that case, I am sorry.
>>>>
>>>>73 Rein W6/PA0ZN

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>>>>Build: 8 succeeded, 0 failed, 0 skipped

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>>>Philip A Covington

>>><http://www.philcovington.com>

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>>>Yahoo! Groups Links

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>>Yahoo! Groups Links

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>Philip A Covington
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>Build: 8 succeeded, 0 failed, 0 skipped
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Replies

| | Author | Date |
|--|--|--------------------------|
| 31 Re: Required computer platform(s) for SharpDSP Hi Rein, The HPSDR is an open source/open hardware project so all design files will be available for download. I do plan to offer boards from bare to fully ... | Philip Covington phil_at_vhmi   | Oct 12, 2005 10:05 pm |

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● Database *From: KD5NWA <KD5NWA@...>*

[kd5nwa](#)

Date: Wed Oct 12, 2005 9:56 pm

[Offline](#)

● Polls *Subject: Re: [Open_HPSSDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...* [Send Email](#)

● Members I installed the beta personal version of VS on my PC, the performance

● Calendar was horrible, but with SharpDevelop my PC was responsive and I had no

● Promote problems zipping around trying to figure out which way is up. Free to

boot, I'm a happy camper!!

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Thanks for the link, I'll check it out tonight.

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At 04:41 PM 10/12/2005, you wrote:

Did you know...

>--- In [Open_High_Performance_SDR@yahoogroups.com](#), KD5NWA <KD5NWA@c...> wrote:

You can set the sort order of messages? Just click on the link in the date column. Your preferences will be remembered, so you don't have to do it again when you return.

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> > By the way does anyone know if this list will allow emails that are and family > > not plain text?

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[page now.](#) >
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 >
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 >
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Cecil Bayona
 KD5NWA
 www.qrpradio.com

I fail to see why doing the same thing over and over and getting the same results every time is insanity: I've almost proved it isn't; only a few more tests now and I'm sure results will differ this time ...

Replies

Author

Date

32 [Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...](#) [nalaregeork](#) Oct 12, 2005
 Well regardless of your choice of dev. env. my point was that you can learn a lot   10:36 pm
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[phil at vhma](#)

● Database *From:* Philip Covington <p.covington@...>

[Offline](#)

● Polls *Date:* Wed Oct 12, 2005 10:05 pm

● Members *Subject:* Re: [Open_HPSSDR] Required computer platform(s) for SharpDSP [Send Email](#)

● Calendar

Hi Rein,

● Promote The HPSDR is an open source/open hardware project so all design files will be available for download. I do plan to offer boards from bare to fully populated. No price has been determined yet as it is just in the prototype stages now.

? Already a member? [Sign in to Yahoo!](#)

73 de phil N8VB

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>Philip A Covington
>http://www.philcovington.com
>
>
>
>Build: 8 succeeded, 0 failed, 0 skipped
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>
>
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```

Build: 8 succeeded, 0 failed, 0 skipped

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Philip A Covington

<http://www.philcovington.com>

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From: "nalaregeork" <nospam@...>

[nalaregeork](#)

Date: Wed Oct 12, 2005 10:36 pm

[Offline](#)

Subject: [Open_HPSPDR] Re: SharpDSP Mini Console for the SoftRock 40 bugfix #1...

[Send Email](#)

Well regardless of your choice of dev. env. my point was that you can learn a lot of C# from Codeproject because I just tested the import feature of SharpDevelop it works and the export feature ain't too bad either.

? Already a member? [Sign in to Yahoo!](#)

I run VS 2003 on an antique 733 256 mb laptop pretty well but, the desktop has a gig in it. I was thinking of getting a Tablet with keyboard for reading on the train I wonder if I can run VS 2003 on one of those? never tried that or knew anybody who tried it? Anbody try that here?

Yahoo! Groups Tips

Did you know...

You can search --- In [Open_High_Performance_SDR@yahoogroups.com](#), KD5NWA the group for <KD5NWA@c...> wrote: older [messages](#).

Yahoo! 360°

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to your friends > I installed the beta personal version of VS on my PC, the
and family to performance
through blogs, > was horrible, but with SharpDevelop my PC was responsive and I had
photos and >
more. [Create](#) >
[your own 360°](#) > Thanks for the link, I'll check it out tonight.
[page now.](#) >

> At 04:41 PM 10/12/2005, you wrote:

> >--- In [Open_High_Performance_SDR@yahoogroups.com](#), KD5NWA <KD5NWA@c...>

```
> >wrote:
> > > it's tough when you don't have a clue. This weekend it's off to
the
> > > bookstore to find some books on .NET, and find some tutorials on
> > > SharpDevelop on the Internet.
> > >
> >
> >Great stuff over at http://www.codeproject.com/ but most of it is
> >Visual Studio 2002/2003 and not much of it geared towards new
users of
> >SharpDevelop. Still there are some interesting articles on Windows
> >Programming using CSharp and after you get the hand of basic C#
> >converting the code to compile under SharpDevelop shouldn't be
> >impossible. Hmm while writing this I tried using the import Project
> >feature of SharpDevelop and it did a pretty good job of importing a
> >single VS 2003 Solution so, codeproject should be a good resource.
> >
> >
> > > By the way does anyone know if this list will allow emails that
are
> > > not plain text?
> >
> >yeah I think it will????
> >
> >
> >
> >
> >
> >
> >
> >Build: 8 succeeded, 0 failed, 0 skipped
> >Yahoo! Groups Links
> >
> >
> >
> >
>
> Cecil Bayona
> KD5NWA
> www.qrpradio.com
>
> I fail to see why doing the same thing over and over and getting
the
> same results every time is insanity: I've almost proved it isn't;
> only a few more tests now and I'm sure results will differ this
time ...
>
```


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[phil at vhmi](#)

● Database *From: "Phil Covington" <p.covington@...>*

[Offline](#)

Date: Wed Oct 19, 2005 2:24 pm

● Polls *Subject: SharpDSP Mini Console with Analog Meter* [Send Email](#)

● Members *Hi all,*

● Calendar

● Promote I just posted a new version of the SharpDSP Mini Console for the SoftRock 40 to my website at:

? Already a member? [Sign in to Yahoo!](#) <http://www.philcovington.com/SDR.html>

Please see my Blog entry for 10-19-2005 for details about this release:

Yahoo!
Groups Tips <http://pcovington.blogspot.com>

Did you know... The default meter face supplied is not calibrated, it is included for demonstration.

You can search the group for older [messages](#). 73 de Phil N8VB

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Replies

Author

Date

34 [SharpDSP Mini Console with Analog Meter](#)

[josephlevy](#)

Oct 19, 2005
5:17 pm

Hello Phil The added feature works fine and looks very nice. while you'r at it can you get the mouse wheel to tune the whole range (on 96 khz sampling) somehow...



[Re: SharpDSP Mini Console with Analog Meter](#)

Phil Covington
[phil at vhmi](#)

Oct 20, 2005
2:07 pm

35 Hi Joe, Unfortunately by default the mousewheel's range when moving the trackbar is +/- 32768 counts. I will have to override the mousewheel event and handle...



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[josephlevy](#)

- Database *From:* "josephlevy" <josephlevy@...>

[Offline](#)

- Polls *Date:* Wed Oct 19, 2005 5:15 pm

[Send Email](#)

- Members *Subject:* re:SharpDSP Mini Console with Analog Meter

Hello Phil

- Calendar The added feature works fine and looks very nice.

- Promote while you'r at it can you get the mouse wheel to tune the whole range (on 96 khz sampling) somehow it does not go all the way to the edges. (it goes from 7021.460 to 7086.995 and not the whole 96 khz span). By the way the idea behind Alex VE3NEA auto I/Q alignment is nice although I didn't manage to get it fully workin yet.

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Awaiting (patiently) for more features to be added.

73 de Joe

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Replies

Author

Date

[Re: SharpDSP Mini Console with Analog Meter](#)

Phil Covington
[phil_at_vhmi](#) Oct 20, 2005
2:07 pm

35Hi Joe, Unfortunately by default the mousewheel's range when moving the trackbar is +/- 32768 counts. I will have to override the mousewheel event and handle...



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[phil at vhmi](#)

● Database *From:* "Phil Covington" <p.covington@...>

[Offline](#)

Date: Thu Oct 20, 2005 2:07 pm

● Polls *Subject:* Re: re:SharpDSP Mini Console with Analog Meter

[Send Email](#)

● Members

● Calendar Hi Joe,

● Promote

Unfortunately by default the mousewheel's range when moving the trackbar is +/- 32768 counts. I will have to override the mousewheel event and handle the updating myself to get the full range. Look for this in a future release.

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Yahoo! Groups Tips

Also, I just uploaded a new version to my website that uses Beppe's analog meter face. It is a really cool graphic.

Did you know... 73 de Phil N8VB

You can search --- In [Open_High_Performance_SDR@yahoogroups.com](#), "josephlevy" the group for <josephlevy@y...> wrote:
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> Hello Phil

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> The added feature works fine and looks very nice.

> while you'r at it can you get the mouse wheel to tune the whole range (on 96 khz sampling) somehow it does not go all the way to the edges.

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> (it goes from 7021.460 to 7086.995 and not the whole 96 khz span).

to your friends

> By the way the idea behind Alex VE3NEA auto I/Q alignment is nice although I didn't manage to get it fully workin yet.

and family

> Awaiting (patiently) for more features to be added.

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>

73 de Joe

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[phil at vhmi](#)

● Database *From: "Phil Covington" <p.covington@...>*

[Offline](#)

Date: Fri Oct 21, 2005 1:52 pm

● Polls *Subject: Nice repository of mathematical software, papers, and databases*

[Send Email](#)

● Members <http://www.netlib.org/>

● Calendar

● Promote " 2.1) What is Netlib?"

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The Netlib repository contains freely available software, documents, and databases of interest to the numerical, scientific computing, and other communities. The repository is maintained by AT&T Bell Laboratories, the University of Tennessee and Oak Ridge National Laboratory, and by colleagues world-wide. The collection is replicated at several sites around the world, automatically synchronized, to provide reliable and network efficient service to the global community."

Yahoo! Groups Tips

Did you know...

Much of SciPy is a thin layer of Python over the above software routines.

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73 de Phil N8VB

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- Database *From: "Phil Covington" <p.covington@...>*
- Polls *Date: Wed Oct 26, 2005 6:00 pm*
- Members *Subject: QSD Model information posted to my blog*

[phil_at_vhmi](#)

 [Offline](#)

[Send Email](#)

- Calendar I've done a little modeling of various QSD circuits. Some of the results can be seen on my blog at <http://pcovington.blogspot.com/>
- Promote

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The interesting part of the data is that the QSD seems to have the least loss when the sampling clock's on time is between 20 - 25% of the sampling clock's total period. This seems to be true in all the different models and configurations that I have simulated.

Yahoo! Groups Tips

73 de Phil N8VB

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[ddatwyler](#)

● Database *From:* "ddatwyler" <dldatwyler@...>

[Offline](#)

Date: Wed Oct 26, 2005 6:22 pm

● Polls *Subject:* A few questions about the Xilinx parts on the proto [Send Email](#)

● Members

● Calendar I have a few Xilinx demo boards, one with Spartan-3, one Spartan-3E, and a Virtex4 FX12. Are these acceptable parts, given I add a few parts like ADC, DAC, some I/O such as SPI or parallel, etc?

● Promote

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I read in the notes so far (blog, too) that the proto board so far has two Xilinx parts on it, I think a Spartan-3 and an FX2. Is this correct to read FX2? I was (still am) unfamiliar with this part.

Yahoo! Groups Tips

And, as yet, I haven't seen if this project is VHDL or Verilog based. My preference (due to experience) is VHDL for now.

Did you know...

And are these QSD commutating, switched, similar to Tayloe?

You can set the sort order of messages? Just click on the link in the date column. Your preferences will be remembered, so you don't have to do it again when you return.

TNX

WR70, Douglas L. Datwyler, PE

Replies

Author

Date

[Re: A few questions about the Xilinx parts on the proto](#)

Philip Covington

[phil_at_vhmi](#)

Oct 26, 2005 6:31 pm

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39Hello, The FX2 part is the Cypress CY7C680013A microcontroller with USB 2.0 The HPSDR_FPGA_USB board uses a Xilinx Spartan 3 FPGA.

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I intend to use VHDL. The...

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[phil_at_vhmi](#)

● Database *From:* Philip Covington <p.covington@...>

[Offline](#)

● Polls *Date:* Wed Oct 26, 2005 6:31 pm

● Members *Subject:* Re: [Open_HPSPDR] A few questions about the Xilinx parts on the proto [Send Email](#)

● Calendar Hello,

● Promote

The FX2 part is the Cypress CY7C680013A microcontroller with USB 2.0

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The HPSPDR_FPGA_USB board uses a Xilinx Spartan 3 FPGA.

I intend to use VHDL.

Yahoo! Groups Tips

The QSD is the tayloe detector.

Did you know...

73 de Phil N8VB

You can set the sort order of messages? Just click on the link in the date column. Your preferences will be remembered, so you don't have to do it again when you return.

On 10/26/05, ddatwyler <dldatwyler@...> wrote:
> I have a few Xilinx demo boards, one with Spartan-3, one Spartan-3E, and a Virtex4 FX12. Are these acceptable parts, given I add a few parts like ADC, DAC, some I/O such as SPI or parallel, etc?
>
> I read in the notes so far (blog, too) that the proto board so far has two Xilinx parts on it, I think a Spartan-3 and an FX2. Is this correct to read FX2? I was (still am) unfamiliar with this part.
>
> And, as yet, I haven't seen if this project is VHDL or Verilog based. My preference (due to experience) is VHDL for now.
>
>

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> And are these QSD commutating, switched, similar to Tayloe?
>

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Philip A Covington
<http://www.philcovington.com>

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From: "Phil Covington" <p.covington@...> [phil_at_vhmi](#)

● Database

Date: Thu Oct 27, 2005 7:30 pm  [Offline](#)

● Polls

Subject: Linear Technology Spice  [Send Email](#)

● Members

SWCad will allow you to output a wave file from the simulation.

● Calendar

● Promote .wave c:\mysound.wav 16 48k V(left) V(right) is the directive.

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The cool part is that you can play the wave files in PowerSDR. As a test I generated two sine 10khz sine waves, one at 0 deg and the other at 90 deg. Then I created a phase error by increasing the 90 deg signal to 110 deg. Using the manual I/Q correction I found that PowerSDR was able to correct for this 20 deg phase imbalance.

Yahoo! Groups Tips

Did you know...

It should be possible to do all kinds of simulations and then actually test them in PowerSDR.

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<http://ltspice.linear.com/software/swcadiii.exe>

73 de Phil N8VB

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From: "Phil Covington" <p.covington@...> [phil_at_vhmi](#)

● Database

Date: Sat Oct 29, 2005 1:53 am

 [Offline](#)

● Polls

Subject: Capacitorless QSD Circuit...

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● Members

...has been posted to my blog at <http://pcovington.blogspot.com/>

● Calendar

● Promote 73 de Phil N8VB

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From: "ddatwyler" <dldatwyler@...> [ddatwyler](#)

● Database

Date: Thu Nov 3, 2005 11:14 pm [Offline](#)

● Polls

Subject: A web site [Send Email](#)

● Members

In reading the dspic_radio newsgroup, they have noted the following web site:

● Calendar

● Promote

<http://www.andraka.com/>

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It appears that the last data added to it was in 2003, but some of the papers look appropriate to this group, as they deal in FPGA radios.

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From: "Edson Pereira" <ewpereira@...> [ewpereira](#)

Date: Sat Nov 19, 2005 2:53 am [Offline](#)

● Database *Subject:* Polyphase FFT

[Send Email](#)

● Polls

● Members

Does anyone know where I can find a good description for the polyphase fft?

? Already a member? [Sign in to Yahoo!](#)

So far I could only find that the polyphase fft can be obtained by getting a block o sampled data. Let's say $4 * N$ where N is the FFT point size; window the block using a raised cosine function; superimpose and add the blocks and compute a fft.

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I would like to know if this description is accurate.

Did you know... Any pointer would be appreciated.

You can search 73, the group for older [messages](#). -- Edson, n1vtn

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Replies

Author





Date

[Re: Polyphase FFT](#)

44Check out this PDF document: "Software Radio Fast Fourier Transforms (FFT's) Polyphase Filtering" http://www.dsparchitectures.com/Flyer_2005.pdf Regards, ...

Per-Tore Aasestrand
[ptaa](#)

Nov 19, 2005
7:39 am

-
[Re: Polyphase FFT](#)
45... It is in "Understanding DSP" 2nd Ed by Richard Lyons, somewhere near page 540 or so if I remember right, in the "DSP tricks" chapter. It is also known by...
.....
- Ramakrishnan Muthukri...
[rkrishnan_m](#)
 
Nov 19, 2005 7:50 am
-
[Re: Polyphase FFT](#)
46 ... The best that I have found, and which did allow me to code a polyphase FFT in my new SDR program, are these : ...
.....
- Alberto I2PHD
[adibene](#)
 
Nov 19, 2005 11:41 am

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From: Per-Tore Aasestrand <ptaa@...>

• Database

Date: Sat Nov 19, 2005 7:38 am  [Offline](#)

• Polls

Subject: Re: [Open_HPSSDR] Polyphase FFT  [Send Email](#)

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http://www.dsparchitectures.com/Flyer_2005.pdf

Regards,

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Per-Tore

Did you know... At 03:53 19.11.2005, you wrote:

You can set the sort order of messages? Just click on the link in the date column. Your preferences will be remembered, so you don't have to do it again when you return.

>Does anyone know where I can find a good description for the polyphase >fft?

>

>So far I could only find that the polyphase fft can be obtained by

>getting a block o sampled data. Let's say 4 * N where N is the FFT

>point size; window the block using a raised cosine function;

>superimpose and add the blocks and compute a fft.

>

>I would like to know if this description is accurate.

>

>Any pointer would be appreciated.

>

>73,

>

>-- Edson, nlvtn

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>Build: 8 succeeded, 0 failed, 0 skipped

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```
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| Per-Tore Aasestrand | E-mail : ptaa@... |
| P.O. Box 1708 Nordnes | Voice : + 47 - 55 32 32 35 |
| N-5816 Bergen, Norway | Mobile : + 47 - 92 04 44 76 |
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From: Ramakrishnan Muthukrishnan <vu3rdd@...> [rkishnan_m](#)

Date: Sat Nov 19, 2005 7:50 am

 [Offline](#)

● Polls

Subject: Re: [Open_HPSSDR] Polyphase FFT

 [Send Email](#)

● Members

On 11/19/05, Edson Pereira <ewpereira@...> wrote:

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● Promote

> Does anyone know where I can find a good description for the polyphase
> fft?

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It is in "Understanding DSP" 2nd Ed by Richard Lyons, somewhere near page 540 or so if I remember right, in the "DSP tricks" chapter. It is also known by some other name.

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> So far I could only find that the polyphase fft can be obtained by
> getting a block o sampled data. Let's say $4 * N$ where N is the FFT
> point size; window the block using a raised cosine function;
> superimpose and add the blocks and compute a fft.

You can search the group for older [messages](#).

> I would like to know if this description is accurate.

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Yes, that's right. you do aliasing in time domain by doing so. Intuitively, uncorrelated signals are reduced in magnitude when we do averaging like this and correlated signals gets boosted and hence are highlighted better in the spectrum.

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Hope this helps.

--

Ramakrishnan VU3RDD

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● Links *From:* Alberto I2PHD <i2phd@...> [adibene](#)

Date: Sat Nov 19, 2005 11:41 am [Offline](#)

● Database *Subject:* Re: Polyphase FFT [Send Email](#)

● Polls

Edson Pereira wrote:

● Members >

● Calendar > Does anyone know where I can find a good description for the polyphase

● Promote > fft?

? Already a member? [Sign in to Yahoo!](#) The best that I have found, and which did allow me to code a polyphase FFT in my new SDR program, are these :

<http://www.wsdmag.com/Articles/Index.cfm?ArticleID=7108&pg=1>

Yahoo! Groups Tips <http://www.rfel.com/download/W03006-Comparison of FFT and PolyDFT Transient Response.pdf>

Did you know...

HTH

You can search the group for 73 Alberto I2PHD older [messages](#).

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From: "Phil Covington" <p.covington@...> [phil_at_vhmi](#)

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Date: Mon Nov 21, 2005 2:37 pm [Offline](#)

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<http://www.web-ee.com/primers/files/VlogIntro.pdf>

http://www.doulos.com/knowhow/verilog_designers_guide/

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http://www.sutherland-hdl.com/on-line_ref_guide/vlog_ref_top.html

Did you know...

<http://www.eecs.harvard.edu/cs141/resources/verilog-tutorial.pdf>

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<http://www.doe.carleton.ca/~shams/97350/Petervrlk.pdf>

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Author

Date

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[nalaregeork](#)

Nov 21, 2005

Thanks Phil good stuff I found some other interesting items regarding FPGA's that someone else may find useful. Being totally entry level where ...



3:46 pm

- 49 [Re: Links to Verilog Tutorials](#) Phil Covington
Hi Al, I have the Xylo board. It works well - the only downside is that no schematics are available and the driver and firmware source code is not available.... [phil_at_vhmi](#) Nov 21, 2005 3:58 pm  
- 50 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 21, 2005 4:22 pm  
Yes I see where you are coming from concerning software and schematics. My Pluto is sufficiently simple enough to not worry about especially at a mere \$59...
- 55 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 22, 2005 3:56 pm  
Oh well I eventually figured out what the core technology of Xilinx is Embedded PowerPC. I was pondering some thoughts about what Altera and Xilinx are doing...
- 56 [Re: Links to Verilog Tutorials](#) [ddatwyler](#) Nov 22, 2005 5:37 pm  
Xilinx has several cores. For all Xilinx, including some CPLD families, you can get PicoBlaze for free. It is more a "2911" like device, if you remember those,...
- 57 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 22, 2005 6:43 pm  
Sorry didn't mean to imply that I thought all Xilinx chips were based on Em PPC I don't think (but don't know) that all (every single one) Altera chips are EM...
- 58 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 23, 2005 7:23 pm  
Here are some interesting items for anybody interested I came across these while researching the Icarus/Mingwin/Eclipse & Verilog plugin setup. It is a quickie...
- 51 [Re: Links to Verilog Tutorials](#) [ddatwyler](#) Nov 21, 2005 7:02 pm  
Does this listing of Verilog tutorials signal a change from VHDL, or does "Open" mean Open Ideas? Just a little confused, since I am considered less than...
- 52 [Re: Links to Verilog Tutorials](#) Robert Kelley
Memec also has some interesting tool offerings. And don't forget Actel... [ac7ke](#) Nov 21, 2005 7:09 pm  

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From: "nalaregeork" <nospam@...> [nalaregeork](#)

● Database

Date: Mon Nov 21, 2005 3:46 pm [Offline](#)

● Polls

Subject: Re: Links to Verilog Tutorials [Send Email](#)

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Thanks Phil good stuff

● Calendar

I found some other interesting items regarding FPGA's that someone

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else may find useful. Being totaly entry level where

FPGS/VHDL/Verilog is concerned I happened upon some inexpensive entry level development materials and hardware.

<http://www.fpgs4fun.com> there is a forum there and they sell some

very nice entry level boards designed by Jean P. Nicolle which are pretty nice. They vary from really inexpensive (Pluto with an Altera Acex chip) to more expensive (Dragon with a Xilinx XC2S100 chipset) I

deceided to go the cheaper route the Pluto but, there is another interesting board I may eventually go with the Xylo with an Altera Cyclone chipset and a lot of builtin IO's (the main value is the

number of IO's) I also went with the Altera because you can compile your apps with the free edition of Quartus II it is feature limited

version and I can't describe the real limits as I am entry level concerning FPGA. I am finding this all quite interesting considering

the reasonble cost of what I have thus far spent. Someday perhaps I expand on it but, at the moment I am spending time with the Verilog tutorial the Phil posted.

Later on Phil and all

73 de Al KC2ORW

In [Open High Performance SDR@yahoogroups.com](#), "Phil Covington" <p.covington@g...> wrote:

>

> <http://www.asic-world.com/verilog/veritut.html>

> <http://www.ece.cmu.edu/~thomas/VSLIDES.pdf>

>

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 > <http://www.eecs.harvard.edu/cs141/resources/verilog-tutorial.pdf>
 >
 > <http://www.doe.carleton.ca/~shams/97350/PetervrlK.pdf>
 >
 > 73 de Phil N8VB
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| | Author | Date |
|--|--|--|
| <p>..... Re: Links to Verilog Tutorials 49Hi Al, I have the Xylo board. It works well - the only downside is that no schematics are available and the driver and firmware source code is not available....</p> | <p>Phil Covington phil_at_vhmi  </p> | <p>Nov 21, 2005 3:58 pm</p> |
| <p>..... Re: Links to Verilog Tutorials 50Yes I see where you are coming from concerning software and schematics. My Pluto is sufficiently simple enough to not worry about especially at a mere \$59...</p> | <p>nalaregeork  </p> | <p>Nov 21, 2005 4:22 pm</p> |
| <p>..... Re: Links to Verilog Tutorials 55Oh well I eventually figured out what the core technology of Xilinx is Embedded PowerPC. I was pondering some thoughts about what Altera and Xilinx are doing...</p> | <p>nalaregeork  </p> | <p>Nov 22, 2005 3:56 pm</p> |
| <p>..... Re: Links to Verilog Tutorials 56Xilinx has several cores. For all Xilinx, including some CPLD families, you can get PicoBlaze for free. It is more a "2911" like device, if you remember those,...</p> | <p>ddatwyler  </p> | <p>Nov 22, 2005 5:37 pm</p> |
| <p>..... Re: Links to Verilog Tutorials 57Sorry didn't mean to imply that I thought all Xilinx chips were based on Em PPC I don't think (but don't know) that all (every single one) Altera chips are EM....</p> | <p>nalaregeork  </p> | <p>Nov 22, 2005 6:43 pm</p> |
| <p>..... Re: Links to Verilog Tutorials 58Here are some interesting items for anybody interested I came across these while researching the Icarus/Mingwin/Eclipse & Verilog plugin setup. It is a quickie...</p> | <p>nalaregeork  </p> | <p>Nov 23, 2005 7:23 pm</p> |

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● Links *From:* "Phil Covington" <p.covington@...> [phil_at_vhmi](#)

Date: Mon Nov 21, 2005 3:56 pm  [Offline](#)

● Database *Subject:* Re: Links to Verilog Tutorials  [Send Email](#)

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● Members Hi Al,

● Calendar I have the Xylo board. It works well - the only downside is that no schematics
● Promote are available and the driver and firmware source code is not available. It is
? Already a not hard to figure out the FX2 to FPGA connection, or to come up with a
member? [Sign schematic on our own though...](#)
[in to Yahoo!](#) The FX2 can be programmed with the open source SDCC.

I use it to test ideas for the HPSDR project .











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Groups Tips 73 de Phil N8VB

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You can set the wrote:
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messages? Just > Thanks Phil good stuff
click on the > I found some other interesting items regarding FPGA's that someone
link in the **date** > else may find useful. Being totaly entry level where
column. Your > FPGS/VHDL/Verilog is concerned I happened upon some inexpensive entry
preferences will > level development materials and hardware.
be remembered, > <http://www.fpgs4fun.com> there is a forum there and they sell some
so you don't > very nice entry level boards designed by Jean P. Nicolle which are
have to do it > pretty nice. They vary from really inexpensive (Pluto with an Altera
again when you > Acex chip) to more expensive (Dragon with a Xilinx XC2S100 chipset) I
return. > deecided to go the cheaper route the Pluto but, there is another
> interesting board I may eventually go with the Xylo with an Altera

Yahoo! 360° > Cyclone chipset and a lot of builtin IO's (the main value is the
> number of IO's) I also went with the Altera because you can compile
Keep connected > your apps with the free edition of Quartus II it is feature limited
to your friends > version and I can't describe the real limits as I am entry level
and family > concerning FPGA. I am finding this all quite interesting considering
through blogs, > the reasonable cost of what I have thus far spent. Someday perhaps I
photos and > expand on it but, at the moment I am spending time with the Verilog
more. [Create](#) > tutorial the Phil posted.

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[page now.](#) > Later on Phil and all
> 73 de Al KC2ORW
>

| | Replies | Author | Date |
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| 50 | <p>Re: Links to Verilog Tutorials</p> <p>Yes I see where you are coming from concerning software and schematics. My Pluto is sufficiently simple enough to not worry about especially at a mere \$59...</p> | <p>nalaregeork</p>   | <p>Nov 21, 2005 4:22 pm</p> |
| 55 | <p>Re: Links to Verilog Tutorials</p> <p>Oh well I eventually figured out what the core technology of Xilinx is Embedded PowerPC. I was pondering some thoughts about what Altera and Xilinx are doing...</p> | <p>nalaregeork</p>   | <p>Nov 22, 2005 3:56 pm</p> |
| 56 | <p>Re: Links to Verilog Tutorials</p> <p>Xilinx has several cores. For all Xilinx, including some CPLD families, you can get PicoBlaze for free. It is more a "2911" like device, if you remember those,...</p> | <p>ddatwyler</p>   | <p>Nov 22, 2005 5:37 pm</p> |
| 57 | <p>Re: Links to Verilog Tutorials</p> <p>Sorry didn't mean to imply that I thought all Xilinx chips were based on Em PPC I don't think (but don't know) that all (every single one) Altera chips are EM....</p> | <p>nalaregeork</p>   | <p>Nov 22, 2005 6:43 pm</p> |
| 58 | <p>Re: Links to Verilog Tutorials</p> <p>Here are some interesting items for anybody interested I came across these while researching the Icarus/Mingwin/Eclipse & Verilog plugin setup. It is a quickie...</p> | <p>nalaregeork</p>   | <p>Nov 23, 2005 7:23 pm</p> |

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Date: Mon Nov 21, 2005 4:22 pm  [Offline](#)

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From: "ddatwyler" <dldatwyler@...> [ddatwyler](#)

Date: Mon Nov 21, 2005 7:02 pm  [Offline](#)

Subject: Re: Links to Verilog Tutorials [Send Email](#)

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I also note that Altera Cyclone2 and Nios2 from Altera are good, as are Spartan3/3E and MicroBlaze from Xilinx. Tools (Quartus2 and Foundation) are free, web available, and cover the devices mentioned so far, for the most part. Both are time limited, but contact with a rep/disti can get you extended trial licenses. Also, look for seminars by the big distis (Arrow and Avnet) for the devices, and some times kits are offered for a lot less than retail price. That's how I got VirtexFX (included Foundation) and Spartan3 (EDK sampler) kits, and Cylcone/Nios. YES, the JTAG cable cost, but are reasonable (I remember \$99 for Altera, and \$125 for Xilinx).

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "Phil Covington" <p.covington@g...> wrote:

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> <http://www.ece.cmu.edu/~thomas/VSLIDES.pdf>

> <http://www.web-ee.com/primers/files/VlogIntro.pdf>

> http://www.doulos.com/knowhow/verilog_designers_guide/

> http://www.sutherland-hdl.com/on-line_ref_guide/vlog_ref_top.html







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| 52 | <p>Re: Links to Verilog Tutorials</p> <p>Memec also has some interesting tool offerings. And don't forget Actel...</p> | <p>Robert Kelley ac7ke</p> <p> </p> | <p>Nov 21, 2005 7:09 pm</p> |
| 53 | <p>Re: Links to Verilog Tutorials</p> <p>And Memec is now part of Avnet, and Avnet offers Xilinx tools and boards. Actel will also send free software on CD for you to try. ... or ... write ... as</p> | <p>ddatwyler</p> <p> </p> | <p>Nov 21, 2005 7:55 pm</p> |
| 54 | <p>Re: Links to Verilog Tutorials</p> <p>Didn't see the expiring renewable license issue till you mentioned it 'Licensing Perpetual (continues to work after expiration) 150 days, request another free...</p> | <p>nalaregeork</p> <p> </p> | <p>Nov 21, 2005 8:16 pm</p> |

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[ac7ke](#)

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From: "nalaregeork" <nospam@...> [nalaregeork](#)

Date: Tue Nov 22, 2005 3:56 pm  [Offline](#)

Subject: Re: Links to Verilog Tutorials  [Send Email](#)

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- 56 [Re: Links to Verilog Tutorials](#) [ddatwyler](#) Nov 22, 2005
Xilinx has several cores. For all Xilinx, including some CPLD families, you can get PicoBlaze for free. It is more a "2911" like device, if you remember those,...
- 57 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 22, 2005
Sorry didn't mean to imply that I thought all Xilinx chips were based on Em PPC I don't think (but don't know) that all (every single one) Altera chips are EM...
- 58 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 23, 2005
Here are some interesting items for anybody interested I came across these while researching the Icarus/Mingwin/Eclipse & Verilog plugin setup. It is a quickie...

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From: "ddatwyler" <dldatwyler@...> [ddatwyler](#)

- Database

Date: Tue Nov 22, 2005 5:35 pm [Offline](#)

- Polls

Subject: Re: Links to Verilog Tutorials [Send Email](#)

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Xilinx has several cores.

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For all Xilinx, including some CPLD families, you can get PicoBlaze for free. It is more a "2911" like device, if you remember those, used as a writable control store state machine. Source code is available. The assembler is also included with the download. For most Xilinx FPGA devices, MicroBlaze is available. It is a 32 bit controller. The EDK provides the processor and developer tools, C++/C compilers, assemblers, linkers, etc. In the FX4 (and I think some other FX type devices) a PowerPC405 is available. Again, the EDK is needed to develop for the PowerPC. EDK comes as evaluation software with some Xilinx/Avnet/Memec and other kits. The compiler is gnu C++/C. I had heard that the latest tools (version 8, mine should be here soon) may be Eclipse based, even for the HDLs. Then again, when you work and know a bit about too many things (jack of all, master of none), you get confused.

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "nalaregeork"

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<nospam@a...> wrote:
> Oh well I eventually figured out what the core technology of Xilinx
> Embedded PowerPC. I was pondering some thoughts about what Altera
> and
> Xilinx are doing as both are using the Cygwin compiler or Mingwin
> and I
> wondered what else there was out there that is free/opensource for
> these chips. I see there is a version of Eclipse for Verilog will
> have
> to take a look at that one...

>
> --- In Open_High_Performance_SDR@yahoo.com, "nalaregeork"
> <nospam@a...> wrote:
> >
> > Yes I see where you are coming from concerning software and
> > schematics.
> > My Pluto is sufficiently simple enough to not worry about
> > especially
> > at
> > a mere \$59 (plus a few more parts) One thing though I wish Jean
> > had
> > recommended the solder in pins right off the top I just ordered
> > that
> > and a ByteBlaster so, I (hopefully) could do a bit more in
> > circuit.
> > Still seems fine for starting. I guess I could have gone with the
> > Spartan 3 dev board but, I don't know how to do that 'on the
> > cheap'
> > Got any advice/recommendations for me or anyone else on working
> > with
> > Xilinx in an inexpensive manner (software wise that is) What is
> > the
> > core on a Spartan 3 I know the Acex is an ARM922T
> > --- In Open_High_Performance_SDR@yahoo.com, "Phil Covington"
>

Replies

Author Date

57 [Re: Links to Verilog Tutorials](#) [nalaregeork](#) Nov 22, 2005
Sorry didn't mean to imply that I thought all Xilinx chips were based on Em PPC I   6:43 pm
don't think (but don't know) that all (every single one) Altera chips are EM....

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From: "nalaregeork" <nospam@...> [nalaregeork](#)

Date: Tue Nov 22, 2005 6:43 pm  [Offline](#)

● Polls

Subject: Re: Links to Verilog Tutorials  [Send Email](#)

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Sorry didn't mean to imply that I thought all Xilinx chips were based on Em PPC I don't think (but don't know) that all (every single one) Altera chips are EM. Arm. If I get anywhere I would be sure to report after all this isn't a simple task and it adds yet one more IDE to deal with would that everything could be done in one place (hmm well there is Emacs ;D)

Later On (likely much later ;D)

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "ddatwyler" <dldatwyler@c...> wrote:

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> Xilinx has several cores.

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the group for >

> For all Xilinx, including some CPLD families, you can get PicoBlaze for free. It is more a "2911" like device, if you remember those, > used as a writable control store state machine. Source code is > available. The assembler is also included with the download. For most

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> Xilinx FPGA devices, MicroBlaze is available. It is a 32 bit

to your friends >

> controller. The EDK provides the processor and developer tools,

and family >

C++/C

through blogs, >

> compilers, assemblers, linkers, etc. In the FX4 (and I think some

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more. [Create](#) >

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[your own 360°](#) >

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[page now.](#) >

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From: "nalaregeork" <nospam@...> [nalaregeork](#)

Date: Wed Nov 23, 2005 7:22 pm [Offline](#)

Subject: Re: Links to Verilog Tutorials [Send Email](#)

Here are some interesting items for anybody interested I came across these while researching the Icarus/Mingwin/Eclipse & Verilog plugin setup. It is a quickie software Radio project using opensource/freeware tools.

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--- In [Open High Performance SDR@yahoogroups.com](#), "nalaregeork" <nospam@a...> wrote:

```

>
> Oh well I eventually figured out what the core technology of Xilinx
is
> Embedded PowerPC. I was pondering some thoughts about what Altera and
> Xilinx are doing as both are using the Cygwin compiler or Mingwin and
I
> wondered what else there was out there that is free/opensource for
> these chips. I see there is a version of Eclipse for Verilog will
have
> to take a look at that one...

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From: Dave Comer <dmcomer@...> [dmcmicro](#)

● Database

Date: Thu Nov 24, 2005 2:57 pm  [Offline](#)

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Subject: Verilog SIMulators  [Send Email](#)

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Phil,

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Thanks for the information on the Verilog tutorials. I have one follow-up question. Do you (or anyone reading the list) have a recommendation of a free simulator open source or otherwise? Not so much as what's out there (I can search for that and am evaluating icarus) but what you have found to good, stable software.

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From: "Phil Covington" <p.covington@...> [phil_at_vhmi](#)

● Database

Date: Sat Nov 26, 2005 2:30 pm [Offline](#)

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Subject: Verilog Free Tools Website [Send Email](#)

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From: "Phil Covington" <p.covington@...> phil_at_vhmi

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Date: Thu Dec 8, 2005 9:32 pm [Offline](#)

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Subject: New Xylo SDR FPGA Mailing List [Send Email](#)

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<http://lists.ae5k.us/pipermail/xylo-sdr-ae5k.us/>

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From: "sis108" <sis108@...>

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Date: Mon Jan 16, 2006 11:39 pm

- Members

Subject: Low Jitter/Phase noise XO's , where to buy 1

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Anyone know where to buy single unit quantities of Low Jitter/Phase noise crystal oscillators similar to the Valpey Fisher units used in the N2PK VNA and Flex Radio's SDR-1000 in the frequency range of 140-200Mhz and 450-500Mhz?

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From: "Marsh, NC7V" <nc7v@...>

Date: Mon Jan 16, 2006 11:58 pm

Subject: Re: [Open_HPSSDR] Low Jitter/Phase noise XO's , where to buy 1

[nc7v](#)

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----- Original Message -----

From: [sis108](#)

To: Open_High_Performance_SDR@yahoogroups.com

Sent: Monday, January 16, 2006 3:38 PM

Subject: [Open_HPSSDR] Low Jitter/Phase noise XO's , where to buy 1

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[nc7v](#)

Date: Tue Jan 17, 2006 12:01 am

[Offline](#)

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Subject: Re: [Open_HPSSDR] Low Jitter/Phase noise XO's , where to buy 1

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● Members Try Flex Radio. They indicated they would sell the 200 mHz XO for, I believe \$60. I am not sure they will have other

● Calendar freq.

Good luck,

Marsh NC7V

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To: Open_High_Performance_SDR@yahoo.com

Sent: Monday, January 16, 2006 3:38 PM

Subject: [Open_HPSSDR] Low Jitter/Phase noise XO's , where to buy 1

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From: "vu3bip" <vu3bip@...> [vu3bip](#)

Date: Fri Feb 3, 2006 11:04 am [Offline](#)

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Subject: Introduction (VU3BIP) [Send Email](#)

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This is VU3BIP Handle - vasu from India.
Software enginner working in the embedded domain.

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Interested in building a SDR for HF operations.

-73

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DE Vasu (VU3BIP)

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From: Anthony Chaprnka <engineer44a@...>

[engineer44a](#)

Date: Sat Feb 18, 2006 12:20 am

[Offline](#)

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From: "Phil Covington" <p.covington@...> [phil at vhmi](#)

Date: Sat Feb 18, 2006 7:50 pm [Offline](#)

Subject: Some archived SDR notes... [Send Email](#)

http://www.philcovington.com/SDR_arch.html

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[phil at vhmi](#)

• Database

From: "Phil Covington" <p.covington@...>

[Offline](#)

• Polls

Date: Tue Feb 21, 2006 2:47 pm

• Members

Subject: HPSDR Progress Report - 02-21-2006

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LTC2208 Mercury Board:

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The HPSDR project is now broken down into various sub-projects. The most important is the Mercury board which contains the LTC2208 130 MSPS ADC. The Mercury board will also contain a FPGA to do the DDC (Digital Down Conversion) function. There will probably be an option to install a Cypress FX2 USB2 microcontroller to make the board stand alone for certain applications. Otherwise the FPGA will connect to the Atlas bus. If you are not sure what Atlas is then see the Xylo list archives.

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Replies

| | Author | Date |
|---|---|-------------------------|
| <p>70 Re: HPSDR Progress Report - 02-21-2006 Phil: This sounds like an excellent plan. Not only do we possibly get a joint version that can be run across desktop and server platforms, we might be able...</p> | Robert McGwier rwmcgwier   | Feb 21, 2006 4:12 pm |
| <p>71 Re: HPSDR Progress Report - 02-21-2006 Hi Bob, One of the things I am after is similar to GNURadio's approach. The reason I want the C# wrapper is that I want to use Iron Python in Mono to create...</p> | Phil Covington phil_at_vhmi   | Feb 21, 2006 4:38 pm |
| <p>72 Re: HPSDR Progress Report - 02-21-2006 Good. Now that I understand your plan more fully, you can absolutely count on my help to make this a success and I will use it myself. It was a good idea...</p> | Robert McGwier rwmcgwier   | Feb 21, 2006 6:54 pm |

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● Database *From:* Robert McGwier <rwmcgwier@...>



Date: Tue Feb 21, 2006 3:37 pm

● Polls *Subject:* Re: [Open_HP_SDR] HP_SDR Progress Report - 02-21-2006

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This sounds like an excellent plan. Not only do we possibly get a joint version that can be run across desktop and server platforms, we might be able to include the C# version inside the MSVS IDE project which would allow us to more easily debug, watch, etc.

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Bob N4HY

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 > 73 de Phil N8VB
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| | Replies | Author | Date |
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From: "Phil Covington" <p.covington@...>

[phil_at_vhmi](#)

Date: Tue Feb 21, 2006 4:37 pm

[Offline](#)

Subject: Re: [Open_HPSSDR] HPSSDR Progress Report - 02-21-2006

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Hi Bob,

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--- In [Open High Performance SDR@yahoogroups.com](#), Robert McGwier <rwmcgwier@...> wrote:

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>
> Phil:
>



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● Database *From:* Robert McGwier <rwmcgwier@...>

[rwmcgwier](#)

● Polls

Date: Tue Feb 21, 2006 5:10 pm



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Subject: Re: [Open_HP_SDR] HP_SDR Progress Report - 02-21-2006



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Good. Now that I understand your plan more fully, you can absolutely count on my help to make this a success and I will use it myself. It was a good idea when I thought it was a port. With the ability to do the flow graphs easily connected by python, OR NOT, it will really be a powerful instrument. We are really going to clean up the DttSP under it now that this has stabilized in our minds. The future goals are to add new capability but not major restructuring. Linux will get a big push now. Also since Jim Lux and Cecil Bayona are providing serious documentation (their first two days work is impressive, having seen it) this should be an aid to all. I am really impressed with the GnuRadio approach. From scratch and a few modules, Eric, Matt, and I built a function 1 mbps GMSK packet system that worked in two days and was polished enough to show in a week. I had to write a few new modules but they were easily added to the whole. From the existing modules, again with a couple new ones I did, I was able to build a stereo decoding FM receiver with total effort about 2 days over a period of 3-4 weeks. It is an impressive approach.

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The LTC2208's may be \$100 but they are worth every dime in my estimation. They are truly impressive parts.

Bob
N4HY

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>> Laziness is the number one inspiration for ingenuity. Guilty as
>>
> charged!
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> Build: 8 succeeded, 0 failed, 0 skipped
> Yahoo! Groups Links
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[n9vv](#)

- Database *From: Ken - N9VV <n9vv@...>*

Date: Tue Feb 21, 2006 9:45 pm

[Offline](#)

- Polls *Subject: Tonight: SDR Linux Users Group 0300z* [Send Email](#)

- Members

From Dan Babcock:

- Calendar

For anyone who might have missed the first announcement. We are having a meeting on TeamSpeak of the "SDR Linux User Group" at 0300Z Wednesday. That is 10:00PM (EST) this evening.

- Promote

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How-to get on Teamspeak tonight:

<http://www.flex-radio.com/teamspeak.htm>

<http://flex-radio.com/forums/viewtopic.php?t=1745>

Yahoo! Groups Tips

Did you know...

----- Original Message -----

You can set the Subject: Re: [Flexradio] On open source development

sort order of Date: Tue, 21 Feb 2006 12:44:49 -0800 (PST)

messages? Just From: Dan Babcock <danbabc@...>

click on the To: FlexRadio Reflector <[FlexRadio@...](#)>

link in the **date**

column. Your I anticipate the first meeting to be "organizational" in nature. I have some definite ideas about the direction I would like to go. preferences will I have some definite ideas about the direction I would like to go. be remembered, But having said that, the success of the group will revolve so you don't around meeting the needs of the group. have to do it

again when you My expectation is that we will have a cross section of users. As return. mentioned in my previous post, I see the objectives as:

[Yahoo! 360°](#)

Assisting anyone that would like to install the Debian Linux distribution.

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Providing assistance to those that have any Linux distribution up and running, are comfortable with it and would like to install the

through blogs, TeamSpeak client, the jsdr core or DttSP-Shell.

photos and

more. [Create your own 360°](#) Developing utilities, installation packages and documentation for the jsdr core and DttSP-Shell.

[page now.](#)

Giving encouragement and feedback to those who are developing {SDR related} Linux applications.

Developing a self-installing suite of programs in a live Linux distro (ala Knoppix and Harv's Hamshack) that would include both application and development software for the SDR-1000. This could be on a CD, DVD or even an OS on a (memory) stick.

Again, this view is from my personal perspective. The actual direction we go remains a fairly large TBD at the moment.

Hopefully you will be able to make it. I am looking forward to your participation.

For anyone who might have missed the first announcement. We are having a meeting on TeamSpeak of the SDR Linux User Group at 0300Z Wednesday. That is 10:00PM this evening.

73

Dan N4XWE

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● Database *From: "sis108" <sis108@...>*

[sis108](#)

● Polls

Date: Fri Feb 24, 2006 12:37 am

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● Members

Subject: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc

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● Calendar

Just wondering how you think closely spaced third order dynamic range and general performance of the HPSDR will compare to a system like the SDR-1000 that just uses direct conversion to audio and a 24 bit sound card. Also other than for receiving signals wider than 10 or 20Khz what advantages does the fast ADC bring.

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Replies

Author

Date

[Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc](#)

Phil Covington
[phil_at_vhmi](#)

Feb 24, 2006
2:19 pm

75... Hello, First, the LTC2208 Mercury board is only a sub-project of the HPSDR project. More than likely there will also be a QSD based board with on board 24...



[Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc](#)

[sis108](#)

Feb 24, 2006
6:52 pm

76 Have you looked at oscillators with a google search for SONET ? There seem to be some that have standard frequencies about 622Mhz if I remember correctly and...



[Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc](#)

Robert McGwier
[rwmcgwier](#)

Feb 25, 2006
7:13 pm

77 Sis108: And as always on all these questions, open universe versus closed universe issues seriously apply. This is usually captured in the phrase: "It...



[Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc](#)

[sis108](#)

Feb 26, 2006
3:49 am

78 The FlexRadio friends are having a discussion on the effects using a very fast rate on the sound card. The group's opinion is yes you will loose some...



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● Database *From: "Phil Covington" <p.covington@...>*

[phil at vhmi](#)

● Polls

Date: Fri Feb 24, 2006 2:18 pm

[Offline](#)

● Members

Subject: Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "sis108"

● Promote >

<sis108@...> wrote:

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> Just wondering how you think closely spaced third order dynamic range
> and general performance of the HPSDR will compare to a system like the
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73 de Phil N8VB

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Replies

Author

Date

- 76 [Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc](#)
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[sis108](#)



Feb 24, 2006
6:52 pm

Robert McGwier
[rwmcgwier](#)



Feb 25, 2006
7:13 pm

[sis108](#)



Feb 26, 2006
3:49 am

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[sis108](#)

- Database *From: "sis108" <sis108@...>*
- Polls *Date: Fri Feb 24, 2006 6:51 pm*

[Send IM](#)

- Members *Subject: Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc* [Send Email](#)

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Have you looked at oscillators with a google search for SONET ?
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 one of the ratings levels.

? Already a member? [Sign in to Yahoo!](#)

--- In [Open High Performance SDR@yahoogroups.com](#), "Phil Covington" <p.covington@...> wrote:

Yahoo! Groups Tips

> --- In [Open High Performance SDR@yahoogroups.com](#), "sis108" <sis108@> wrote:

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Keep connected to your friends and family

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photos and more. [Create your own 360° page now.](#) > of the critical items for the LTC2208 Mercury board is the encode > clock. It needs to be stable and *extremely* low phase noise to > get > good performance. The current QSD designs start to have degraded > performance above 20-30 MHz. The input bandwidth of the LTC2208 is > 700 MHz so it should be possible to cover 2 meters and maybe even > 40 > cm. This is where the encode clock quality will make or break the > system. > > 73 de Phil N8VB >

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• Database *From: Robert McGwier <rwmcgwier@...>*

[rwmcgwier](#)

• Polls *Date: Sat Feb 25, 2006 6:12 pm*

 [Offline](#)

• Members *Subject: Re: [Open_HPSDR] Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc*

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• Calendar *Sis108:*

• Promote And as always on all these questions, open universe versus closed universe issues seriously apply. This is usually captured in the phrase:

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The dynamic range you will see in a narrow band channel constructed by optimal decimation and filtering will, in large part, be determined by what is in front of the A/D and the environment. If you bandpass (say) 20 meters and then do optimal decimation/filtering to get a 3 kHz channel, I am expecting this receiver will have IMD DR3 in excess of 80 dB. Again, in the "it depends" department, this will be impacted by the existence of gain management or lack thereof and the quality of the same. It could be close to 90 dB. This is due almost entirely to the extremely high quality of this A/D if it meets its specifications. A place that I am expecting the QSD/SDR-1000 to outperform this system in a meaningful way is on blocking dynamic range. I think this design (Mercury) will be able get within 10 dB of the IMD-DR3, have good IP3, etc.

The things you gain by using a circuit can really make up for the loss of 10-15 dB of dynamic range. It depends on your application. For my applications, I might want 22 narrow band channels all at once anywhere in the 20 meter band. I cannot do that with the SDR-1000. I will be able to do it with Lionheart, Mercury, etc. etc. glued together in a coherent package with our effort.

Yahoo! 360°

Keep connected to your friends and family If you want a contest super station radio, the SDR-1000 will have better receiver numbers if the wideband LTC2208 based receiver is used but it is not ergonomically a contest super radio now. To get numbers that are almost as good as the SDR-1000 (but are NOT as good) but

through blogs, probably are good enough, and in a much better package ergonomically photos and speaking, you need to spend \$13,000 and buy a IC7800 from Icom. more. [Create your own 360° page now.](#)

It would pay to squeeze your request into a smaller universe, one that we see the edges of in a few light seconds of travel.

Bob

Phil Covington wrote:



```
> --- In Open\_High\_Performance\_SDR@yahoo.com, "sis108"
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--

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Laziness is the number one inspiration for ingenuity. Guilty as charged!

Replies

Author Date

78 [Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc](#) [sis108](#) Feb 26, 2006
The FlexRadio friends are having a discussion on the effects using a very fast rate on the   3:49 am
sound card. The group's opinion is yes you will loose some...

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From: "sis108" <sis108@...>

[sis108](#)

Date: Sun Feb 26, 2006 3:49 am

 [Send IM](#)

Subject: [Open_HPSSDR] Re: 16 bit LTC2208 vrs 24 bit sound card dynamic range etc

 [Send Email](#)

The FlexRadio friends are having a discussion on the effects using a very fast rate on the sound card. The group's opinion is yes you will loose some performance. This brings up a question of how much blocking dynamic range is enough. One guy who has done a lot of testing on the receiver basically said on 160 meters a signal would have to be 20db louder than he has ever come across to effect the even the weakest signals.

Yahoo! Groups Tips

I think most people want the wider width for the pan adaptor display, or maybe a 2nd in band receiver >48khz away.

Did you know...

It looks like the open HPSDR could have the best of both worlds with a high dynamic range main receiver <24 bit> for the main receiver and a 2nd fast 16bit AGC for a very wide pan adaptor and multiple auxiliary receivers. That would be a very impressive set up in-dead.

click on the

I am not saying that is the way to go, or one is better than the other. I mainly read these posts trying to learn. I have been following the developments as close as I can and I am very impressed with the work of the group.

so you don't have to do it

--- In [Open High Performance SDR@yahoo.com](mailto:Open_High_Performance_SDR@yahoo.com), Robert McGwier <rwmcgwier@...> wrote:

again when you return.

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>
> Sis108:

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more. [Create](#) >
[your own 360°](#) > The dynamic range you will see in a narrow band channel constructed by
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• Links *From:* Eric Blossom <eb@...> eb@...
 • Database *Date:* Sun Feb 26, 2006 4:29 am [Send Email](#)
 • Polls *Subject:* Cool stuff!

- Members

• Calendar It looks like you folks are doing great work! Is there one place I
 • Promote can look to get a description of all the boards under design or being
 considered, as well as the h/w and s/w architecture, protocols, etc?
 (I'm trying to get up to speed quickly.)

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 Eric

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Author

Date

81 [Re: Cool stuff!](#)
 Phil Covington
[phil_at_vhmi](#) Feb 26, 2006 4:13 pm
 ... Hi Eric, Probably the best place to get info is the Xylo list. <http://xylo-sdr.ae5k.us/> has some info about the list and project. We should probably set...

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[sis108](#)

• Database *From:* "sis108" <sis108@...>

 [Send IM](#)

• Polls *Date:* Sun Feb 26, 2006 3:45 pm

 [Send Email](#)

• Members *Subject:* Sonet low noise oscillator link
Found what I was thinking about earlier.

• Calendar <http://www.elecdesign.com/Articles/ArticleID/1332/1332.html>

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http://www.cmac.com/mt/news/CM1_053_A_CFPV_5.html

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Author

Date

82 [Re: Sonet low noise oscillator link](#)

Phil Covington
[phil_at_vhmi](#)

Feb 26, 2006
4:18 pm

... Thanks for the links. The second link is interesting because they are talking about jitter less than 0.5pS. I wonder if we would degrade the jitter by...















83 [\[OT\] FPGA development kits](#)

Ken N9VV
[n9vv](#)

Feb 26, 2006
4:39 pm

F.Y.I. this is for us new guys who aren't familiar with FPGA development kits. Nice overview and gives price comparisons: ...

- [Re: \[OT\] FPGA development kits](#)
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- Robert McGwier
[rwmcgwier](#)
 
 Feb 26, 2006 6:50 pm
- [\[OT\] FPGA development kits - new link](#)
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[n9vv](#)
 
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 Feb 26, 2006 6:57 pm
- [Re: Sonet low noise oscillator link](#)
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- Robert McGwier
[rwmcgwier](#)
 
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- [Re: Sonet low noise oscillator link](#)
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- [sis108](#)
 
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From: "Phil Covington" <p.covington@...> [phil at vhmi](#)

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Date: Sun Feb 26, 2006 4:12 pm

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Subject: Re: Cool stuff!

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--- In [Open High Performance SDR@yahoogroups.com](#), Eric Blossom

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> 73

> K7GNU

> Eric

>

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Probably the best place to get info is the Xylo list.

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We should probably set up a Wiki to document the project.

73 de Phil N8VB

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● Database *From:* "Phil Covington" <p.covington@...> [phil_at_vhmi](#)
Date: Sun Feb 26, 2006 4:16 pm [Offline](#)
Subject: Re: Sonet low noise oscillator link [Send Email](#)

● Members --- In [Open_High_Performance_SDR@yahoogroups.com](#), "sis108"

● Calendar <sis108@...> wrote:

● Promote >

> Found what I was thinking about earlier.
 > <http://www.elecdesign.com/Articles/ArticleID/1332/1332.html>
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date Thanks for the links. The second link is interesting because they are column. Your talking about jitter less than 0.5pS. I wonder if we would degrade preferences willthe jitter by dividing the 622.08 MHz clock by 5 to get 124.416 MHz be remembered,for the encode clock (will we add jitter by dividing)?

so you don't have to do it 73 de Phil N8VB again when you return.

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 sis108 Feb 27, 2006 12:42 am
 
- 92 [Re: Sonet low noise oscillator link](#)
 You can find lots to read on with google. It is often called "bandpass sampling" or "folding the spectrum". What typically happens in those cases where you...
 Robert McGwier [rwmcgwier](#) Feb 27, 2006 2:11 am
 

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● Database *From:* Ken N9VV <n9vv@...>

Date: Sun Feb 26, 2006 4:39 pm [Offline](#)

● Polls *Subject:* [OT] FPGA development kits [Send Email](#)

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|---|---|---------------------------------|
| <p>84... Re: [OT] FPGA development kits This link did not work for me. Bob -- AMSAT VP Engineering. Member: ARRL, AMSAT-DL, TAPR, Packrats, NJQRP/AMQRP, QRP ARCI, QCWA, FRC. ARRL SDR Wrk Grp...</p> | <p>Robert McGwier rwmcgwier  </p> | <p>Feb 26, 2006 6:50 pm</p> |
| <p>88 [OT] FPGA development kits - new link Oops... my bad. Here is the link again: http://www.elecdesign.com/Articles/ArticleID/12005/12005.html de ken...</p> | <p>Ken N9VV n9vv  </p> | <p>Feb 26, 2006 8:39 pm</p> |

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[rwmcgwier](#)

• Database *From:* Robert McGwier <rwmcgwier@...>

[Offline](#)

• Polls *Date:* Sun Feb 26, 2006 6:01 pm

• Members *Subject:* Re: [Open_HPSDR] [OT] FPGA development kits [Send Email](#)

• Calendar Ken N9VV wrote:

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> FPGA development kits. Nice overview and gives price

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Bob

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Laziness is the number one inspiration for ingenuity. Guilty as charged!

Replies

Author **Date**

Ken
N9VV Feb 26, 2006
8:39 pm

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[kd5nwa](#)

● Database *From:* KD5NWA <KD5NWA@...>

 [Offline](#)

● Polls *Date:* Sun Feb 26, 2006 6:57 pm

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Subject: Re: [Open_HPSSDR] Re: Sonet low noise oscillator link

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>--- In [Open High Performance SDR@yahoogroups.com](#), "sis108"

><sis108@...> wrote:

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> > Because they are "standard" products they may be easier to get.

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> > Just something to consider.

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more. [Create](#)

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>for the encode clock (will we add jitter by dividing)?

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>73 de Phil N8VB

>







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> because .5 ps jitter is a smaller part of the lower output frequency.
>

Yahoo! Groups Tips

What I was concerned about is the circuit that does the dividing adding more jitter to the clock in the dividing process than what you started out with. Theoretically division should reduce the jitter, but we are talking about less than 0.5 pS to begin with.

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Replies

Author

Date

89 [Re: Sonet low noise oscillator link](#)

[kd5nwa](#)

Feb 26, 2006

That is why I said "well", one would have to be very carefull and pay attention to all details to not make the jitter worse. .5 ps jitter is excellent, and...



9:25 pm



[Re: Sonet low noise oscillator link](#)

Robert McGwier

90That is why they make evaluation models for these things so you can learn good practice. Bob ... -- AMSAT VP Engineering. Member: ARRL, AMSAT-DL, TAPR,...

[rwmcgwier](#)

Feb 26, 2006
10:38 pm



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From: "Phil Covington" <p.covington@...>

[phil at vhmi](#)

Date: Sun Feb 26, 2006 8:33 pm

 [Offline](#)

● Database

Subject: Possible encode clock for LTC2208...

 [Send Email](#)

● Polls

● Members

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[<http://www.crystek.com/spec-sheets/CCO-083_085.pdf>](http://www.crystek.com/spec-sheets/CCO-083_085.pdf)

[<http://www.crystek.com/spec-sheets/CCPD-912.pdf>](http://www.crystek.com/spec-sheets/CCPD-912.pdf)

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73 de Phil N8VB

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[n9vv](#)

● Database *From:* Ken N9VV <n9vv@...>

Date: Sun Feb 26, 2006 8:38 pm

[Offline](#)

● Polls *Subject:* [OT] FPGA development kits - new link

[Send Email](#)

● Members Oops... my bad. Here is the link again:

● Calendar <http://www.elecdesign.com/Articles/ArticleID/12005/12005.html>

● Promote de ken

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Robert McGwier wrote:
> This link did not work for me.

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[kd5nwa](#)

● Database *From:* KD5NWA <KD5NWA@...>

[Offline](#)

● Polls *Date:* Sun Feb 26, 2006 9:23 pm

● Members *Subject:* Re: [Open_HP_SDR] Re: Sonet low noise oscillator link

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That is why I said "well", one would have to be very carefull and pay attention to all details to not make the jitter worse. .5 ps jitter is excellent, and they probably have a whole set of conditions in order to make it so low.

● Promote

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Phil Covington wrote:

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>--- In [Open_High_Performance_SDR@yahoogroups.com](#), KD5NWA <KD5NWA@...>

>wrote:

>

Did you know...

>>If you design the circuit well, you will reduce the jitter. Mainly because .5 ps jitter is a smaller part of the lower output frequency.

You can set the sort order of

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messages? Just

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>What I was concerned about is the circuit that does the dividing adding more jitter to the clock in the dividing process than what you

preferences will

>started out with. Theoretically division should reduce the jitter,

be remembered,

>but we are talking about less than 0.5 pS to begin with.

so you don't

>

have to do it

>

again when you

>73 de Phil N8VB

return.

>

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photos and >Build: 8 succeeded, 0 failed, 0 skipped
more. [Create](#) >Yahoo! Groups Links
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Replies

Author

Date



[Re: Sonet low noise oscillator link](#)

Robert McGwier

90That is why they make evaluation models for these things so you can learn good practice. Bob ... -- AMSAT VP Engineering. Member: ARRL, AMSAT-DL, TAPR,...

[rwmcgwier](#)

Feb 26, 2006
10:38 pm



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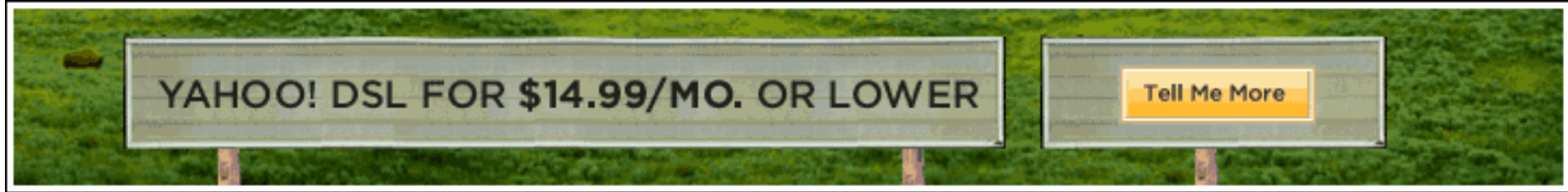
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From: Robert McGwier <rwmcgwier@...>

[rwmcgwier](#)

Date: Sun Feb 26, 2006 9:45 pm

[Offline](#)

Subject: Re: [Open_HPSSDR] Re: Sonet low noise oscillator link

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That is why they make evaluation models for these things so you can learn good practice.

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KD5NWA wrote:

> That is why I said "well", one would have to be very carefull and pay
> attention to all details to not make the jitter worse. .5 ps jitter is
> excellent, and they probably have a whole set of conditions in order to
> make it so low.

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> Phil Covington wrote:

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>> --- In [Open_High_Performance_SDR@yahoogroups.com](#), KD5NWA <KD5NWA@...>
>> wrote:

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>>> If you design the circuit well, you will reduce the jitter. Mainly
>>> because .5 ps jitter is a smaller part of the lower output frequency.

>> What I was concerned about is the circuit that does the dividing
>> adding more jitter to the clock in the dividing process than what you
>> started out with. Theoretically division should reduce the jitter,

>> but we are talking about less than 0.5 pS to begin with.

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>> 73 de Phil N8VB

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>> Build: 8 succeeded, 0 failed, 0 skipped

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[sis108](#)

● Database *From:* "sis108" <sis108@...>

Date: Mon Feb 27, 2006 12:41 am

 [Send IM](#)

● Polls *Subject:* Re: Sonet low noise oscillator link

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Looks like I misunderstood the ADC bandwidth was 700 Mhz so I was thinking that was the encode rate. I forgot you can encode lower than the RF input. Kind of like harmonics of the encode clock mix with the rf in. That's why I passed on the 622Mhz link. Other Stratum 3 specks are for around 150mhz. I don't remember a 122Mhz listing. I started looking at these when I was thinking about DDS reference clocks for chips faster than the AD9851. They make a nice 500Mhz DDS but these are too fast for that too.

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--- In [Open_High_Performance_SDR@yahoogroups.com](#), "Phil Covington"

Did you know... <p.covington@...> wrote:
>

You can search > --- In [Open_High_Performance_SDR@yahoogroups.com](#), "sis108"

the group for > <sis108@> wrote:

older [messages](#). > >

> > Found what I was thinking about earlier.

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> > <http://www.elecdesign.com/Articles/ArticleID/1332/1332.html>

Keep connected > > May want to look for this also

to your friends > > http://www.cmac.com/mt/news/CM1_053_A_CFPV_5.html

and family > >

through blogs, > > The sonet Stratum 3 spec covers low jitter and a 622.08 Mhz clock.

photos and > >

more. [Create](#) > > Because they are "standard" products they may be easier to get.



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[page now.](#) > > Just something to consider.

> >
> Thanks for the links. The second link is interesting because they are
> talking about jitter less than 0.5ps. I wonder if we would degrade

> the jitter by dividing the 622.08 MHz clock by 5 to get 124.416 MHz
> for the encode clock (will we add jitter by dividing)?
>
> 73 de Phil N8VB
>

Replies

| | | Author | Date |
|----|---|--|-------------------------|
| 92 | Re: Sonet low noise oscillator link You can find lots to read on with google. It is often called "bandpass sampling" or "folding the spectrum". What typically happens in those cases where you... | Robert McGwier rwmcgwier   | Feb 27, 2006 2:11 am |

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[rwmcgwier](#)

● Database *From:* Robert McGwier <rwmcgwier@...>

 [Offline](#)

● Polls *Date:* Mon Feb 27, 2006 1:50 am

● Members

Subject: Re: [Open_HPSSDR] Re: Sonet low noise oscillator link

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You can find lots to read on with google. It is often called "bandpass sampling" or "folding the spectrum". What typically happens in those cases where you are bandpass sampling is the noise figure rises a lot at the "undersampled" or bandpass sampled or folded frequencies. What must be true is that the aperture of the A/D must "recognize" those frequencies and not overly distort them and the <<bandwidth >> of the signal of interest must be less than nyquist frequency.

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Bob N4HY

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sis108 wrote:
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> clocks for chips faste than the AD9851. They make a nice 500Mhz DDS
> but these are too fast for that too.

--- In [Open High Performance SDR@yahoogroups.com](#), "Phil Covington"
> <p.covington@...> wrote:

Yahoo! 360° >> --- In [Open High Performance SDR@yahoogroups.com](#), "sis108"

>> <sis108@> wrote:

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and family >>> <http://www.elecdesign.com/Articles/ArticleID/1332/1332.html>

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more. [Create](#) >>> http://www.cmac.com/mt/news/CM1_053_A_CFPV_5.html
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[page now.](#) >>> The sonet Stratum 3 spec covers low jitter and a 622.08 Mhz clock.
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>>> Just something to consider.
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>>>
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From: "Phil Covington" <p.covington@...>

[phil at vhmi](#)

● Database

Date: Thu Mar 2, 2006 1:45 pm

 [Offline](#)

● Polls

Subject: ***Important Notice: New mailing list ...***

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● Members

Hello all,

● Calendar

● Promote

Effective immediately the HPSDR and and various Xylo projects have been combined into one project. We have a new domain and website (<<http://hpsdr.org>>) as well as a new mailing list.

? Already a member? [Sign in to Yahoo!](#)

For those of you already subscribed to the Xylo SDR list, you will be automatically subscribed to the HPSDR list. The Xylo SDR list will cease to exist shortly thereafter.

Yahoo! Groups Tips

Did you know...

Because this change consolidates these projects, the Open High Performance SDR Yahoo list is redundant. I am asking all of you who are subscribed to this list AND who are not subscribed to the Xylo SDR list to subscribe to the new HPSDR list.

older [messages](#).

To subscribe to the HPSDR list go to:

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<<http://hpsdr.org/reflector.html>>

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Again: If you are already subscribed to the Xylo SDR list you do not have to do anything - you will automatically be migrated to the new HPSDR list. This only affects those who are not subscribed to the Xylo SDR list and are subscribed to this list.

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This yahoo list will be deleted after a period of time and the new HPSDR list will take its place.

Thanks ,

Phil Covington N8VB

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From: "ae5k_list" <don@...> [ae5k_list](#)

● Database *Date:* Thu Mar 2, 2006 4:46 pm [Offline](#)

● Polls *Subject:* Another Announcement [Send Email](#)

● Members

The following is a composite of remarks/etc. of several major contributors to this group. Although we have already made the big change with the acquisition of a domain name (hpsdr.org) and completed the move of the two reflectors to the new HPSDR reflector, I thought that some insight into the rationale behind the change would be welcome by many.

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This message is being sent to three reflectors: the Yahoo HPSDR reflector, the old Xylo-SDR reflector, and the new combined HPSDR reflector. This will be the last message on the Xylo-SDR reflector.

Did you know... Don, AE5K

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IMPORTANT ANNOUNCEMENT

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The decision was made by major contributors to combine the Xylo-SDR reflector with Phil N8VB's, High Performance SDR (HPSDR) reflector.

This makes technical as well as administrative sense. Technically both projects are going to use the same Altas backplane and we would like to share code and expertise between the groups.

From an administration perspective, folks are becoming confused as to what group does what and the goals of each project.

Since the "Xylo" name is that of a commercial product no longer directly related to our projects, we have decided to drop the Xylo name and use HPSDR for the combined project.

Web site <http://hpsdr.org> has been set up which will be the single focal point for the project from now on. (It needs work, but you'll see changes soon.)

A decision regarding SVN (Subversion version control) has not been made yet and for the time being, www.hamsdr.com will continue to be used.

All of us working on the Xylo project are very excited about combining the two projects and we have great expectations in relation to the new ideas, techniques and technologies we will be investigating in the coming months.

Many thanks to all those that have assisted in the rapid amalgamation of the two projects and apologies to anyone who's toes we have trod on in the process!

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